

FIG. 1A

FIG. 1A
FIG. 1B

FIG. 1

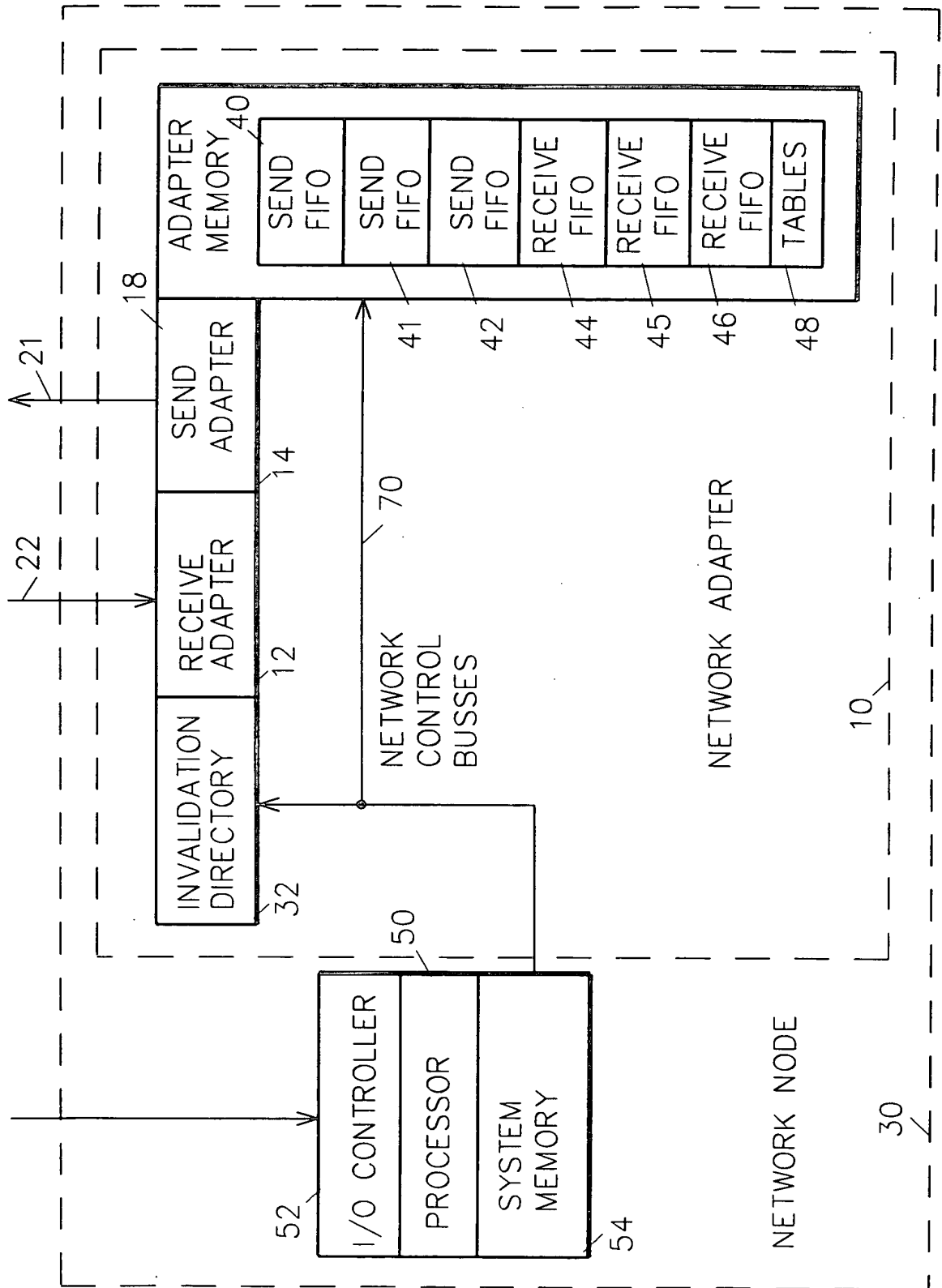


FIG. 1B

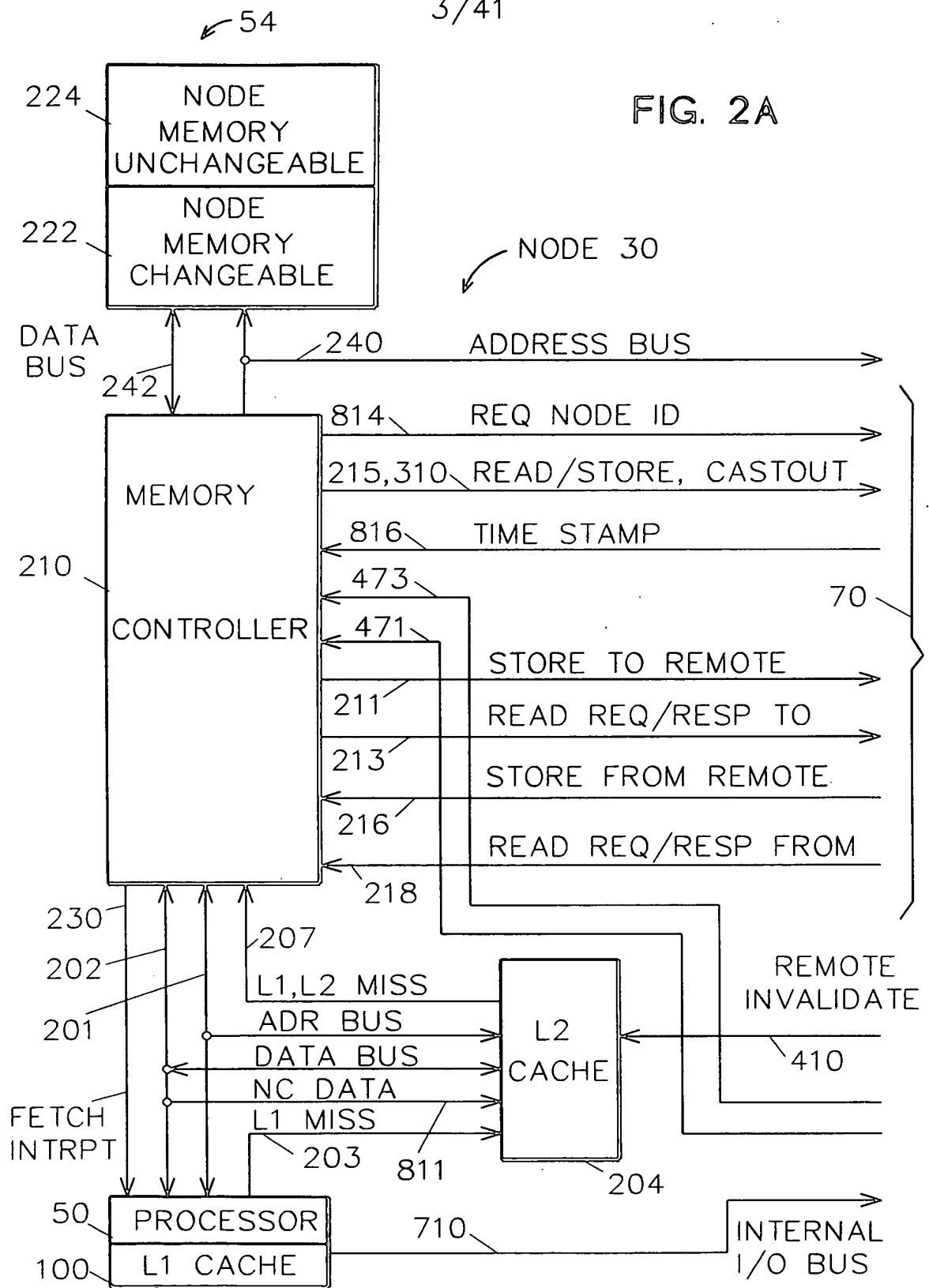


FIG. 2A FIG. 2B

FIG. 2

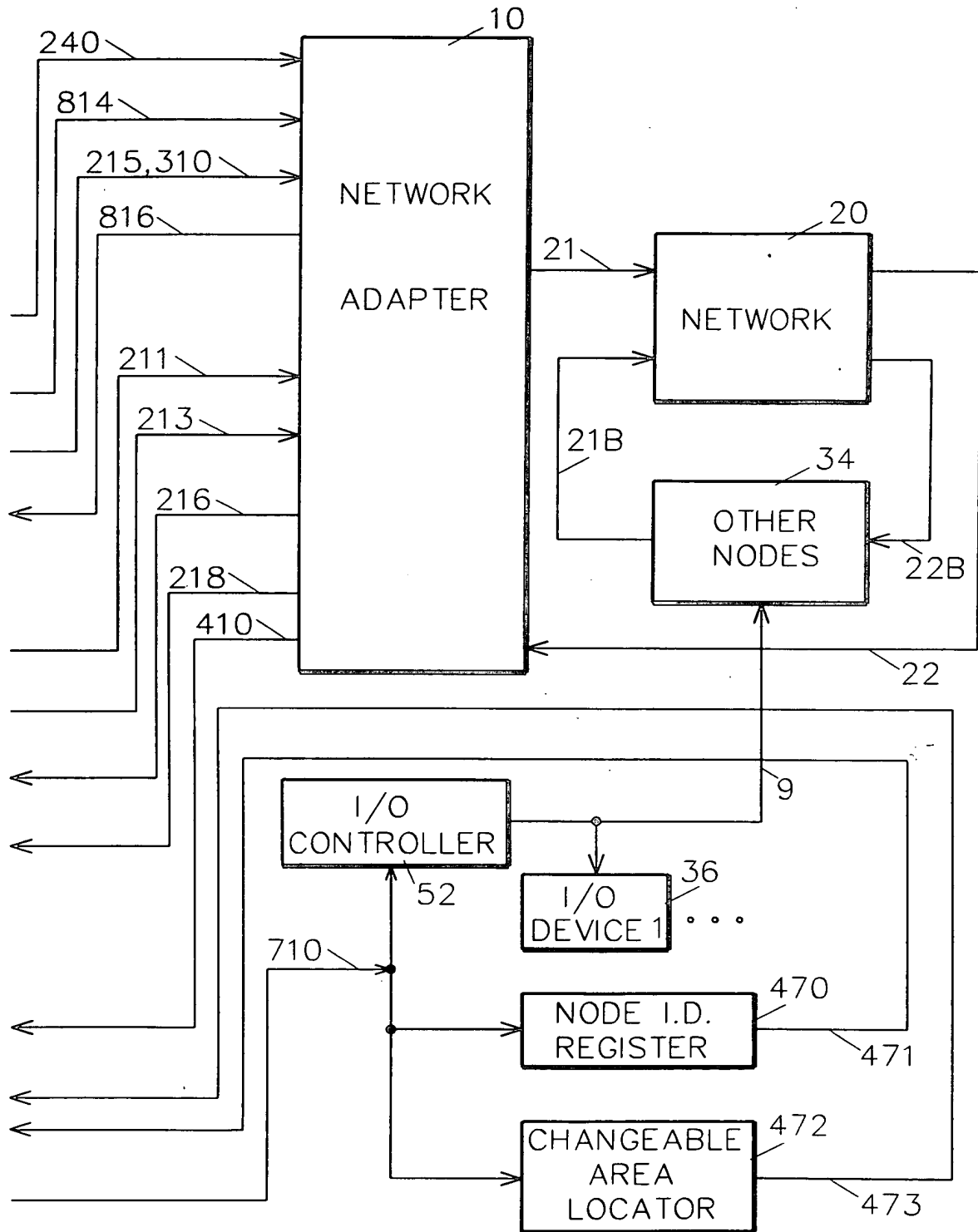


FIG. 2B

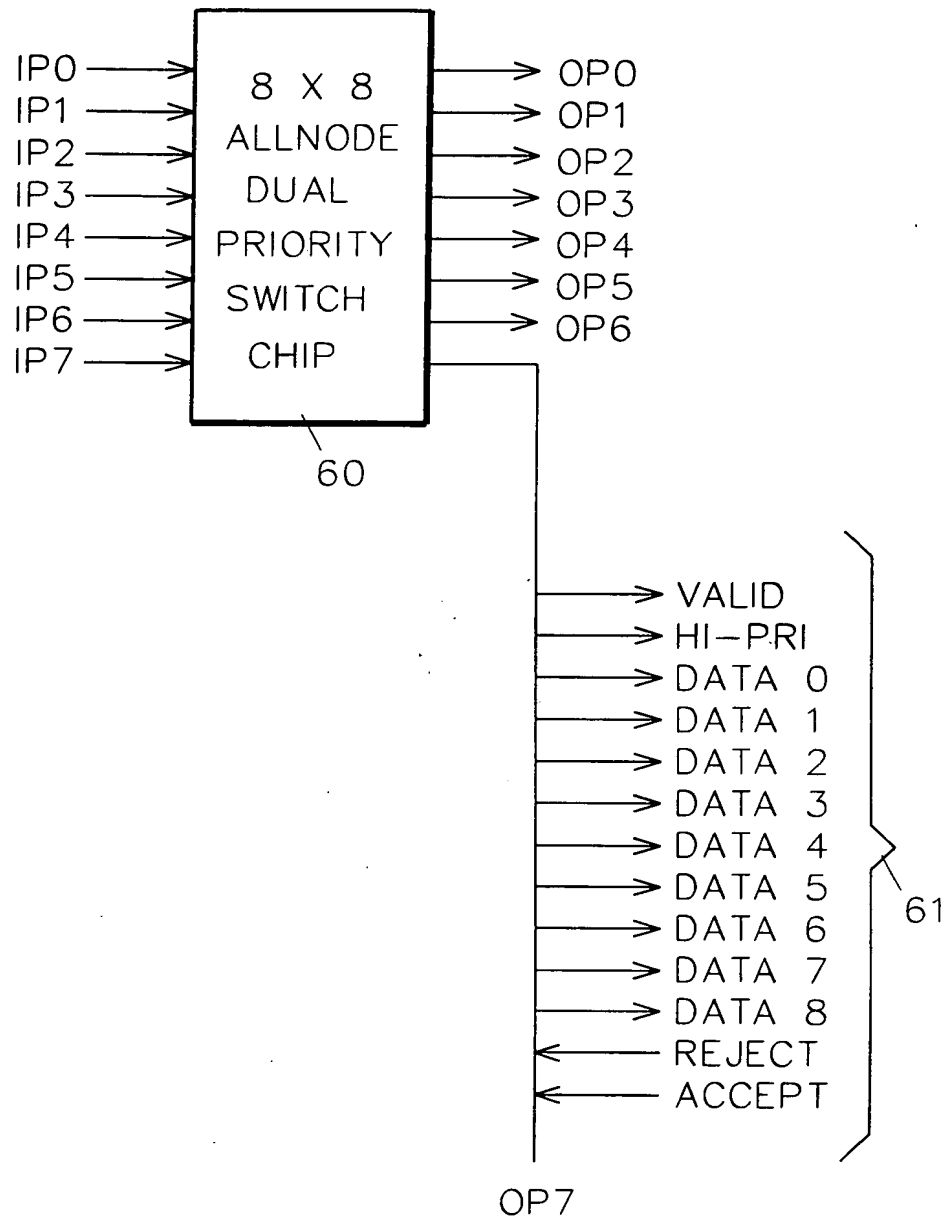
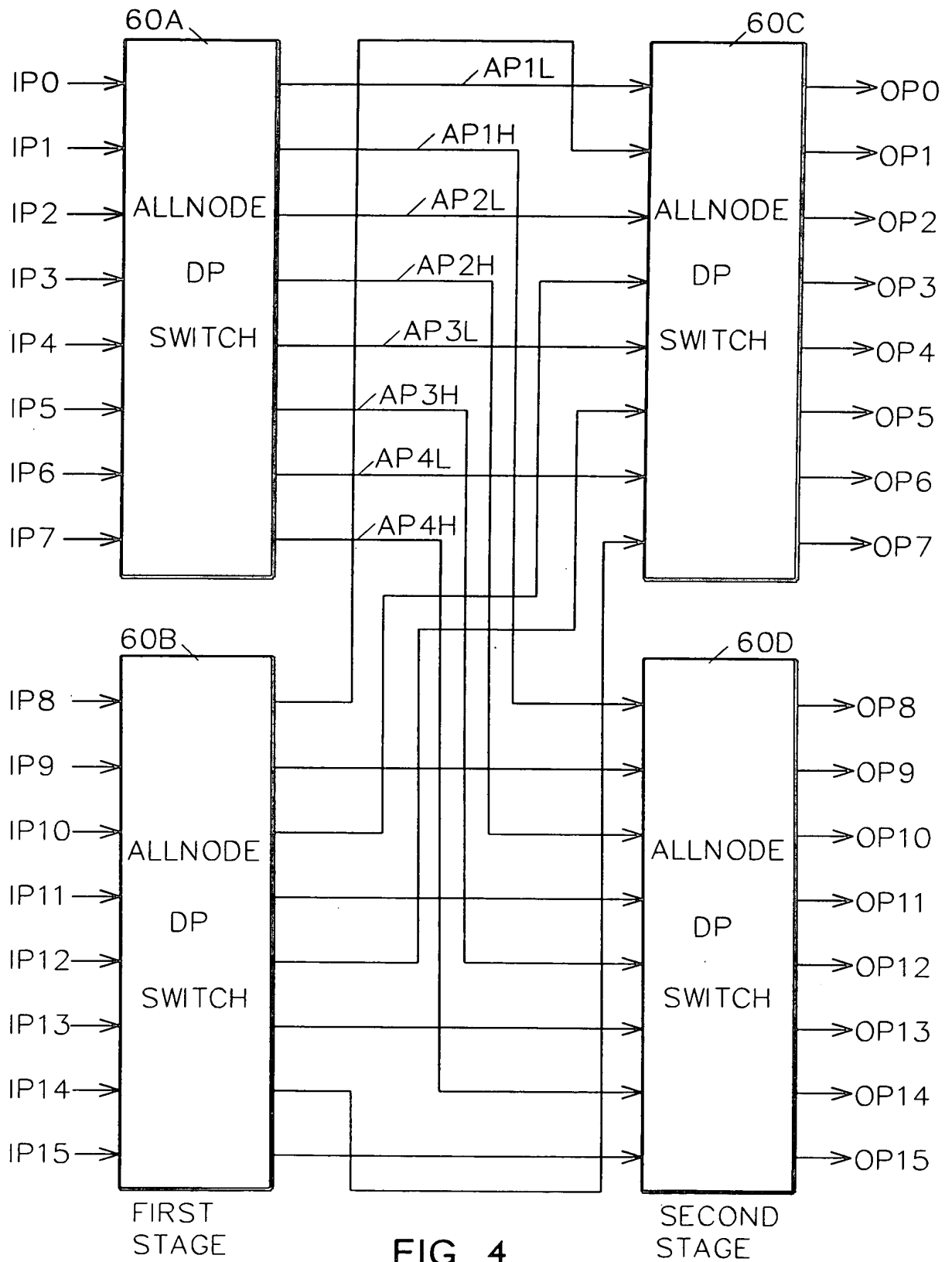


FIG. 3



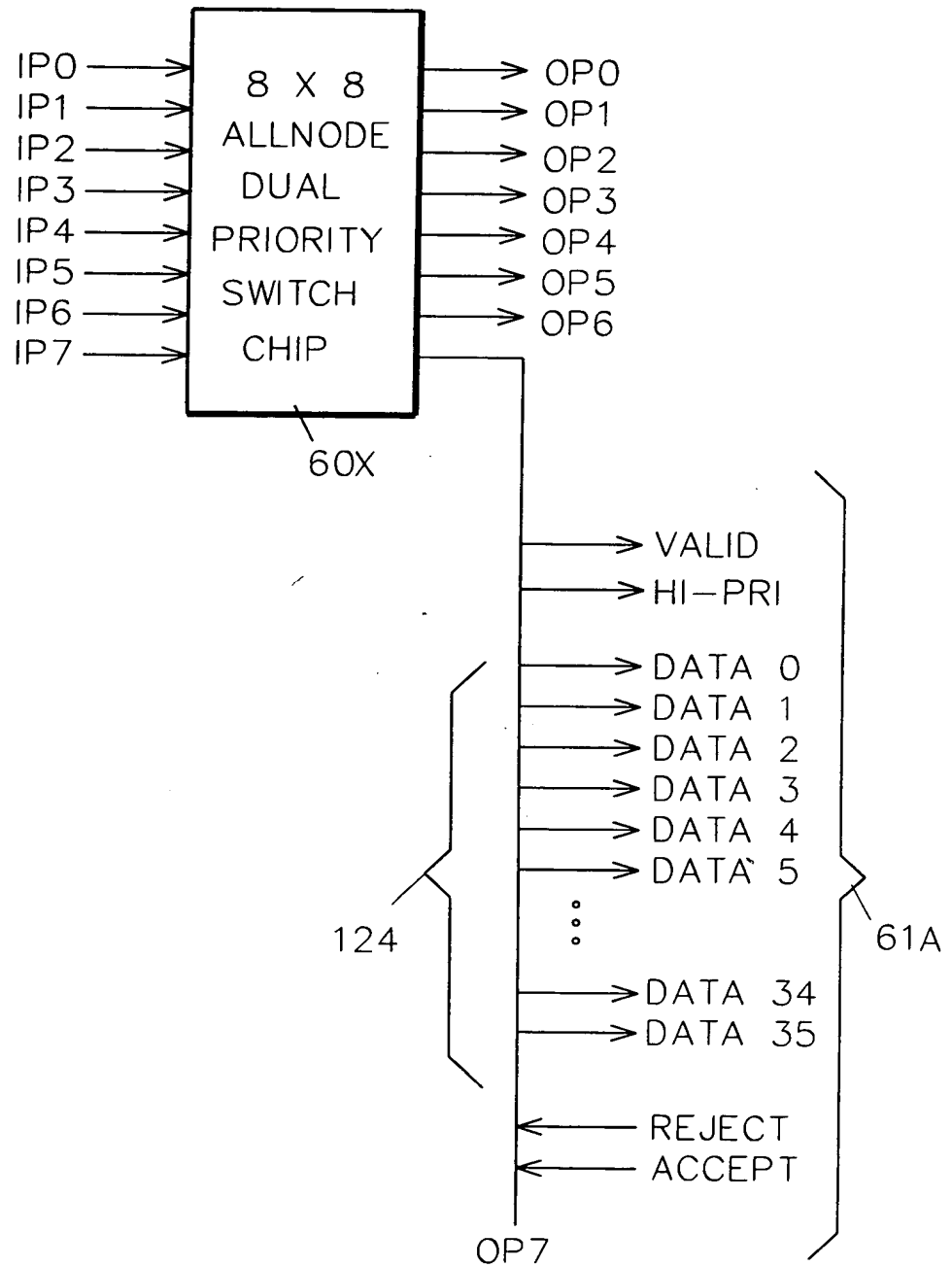


FIG. 5

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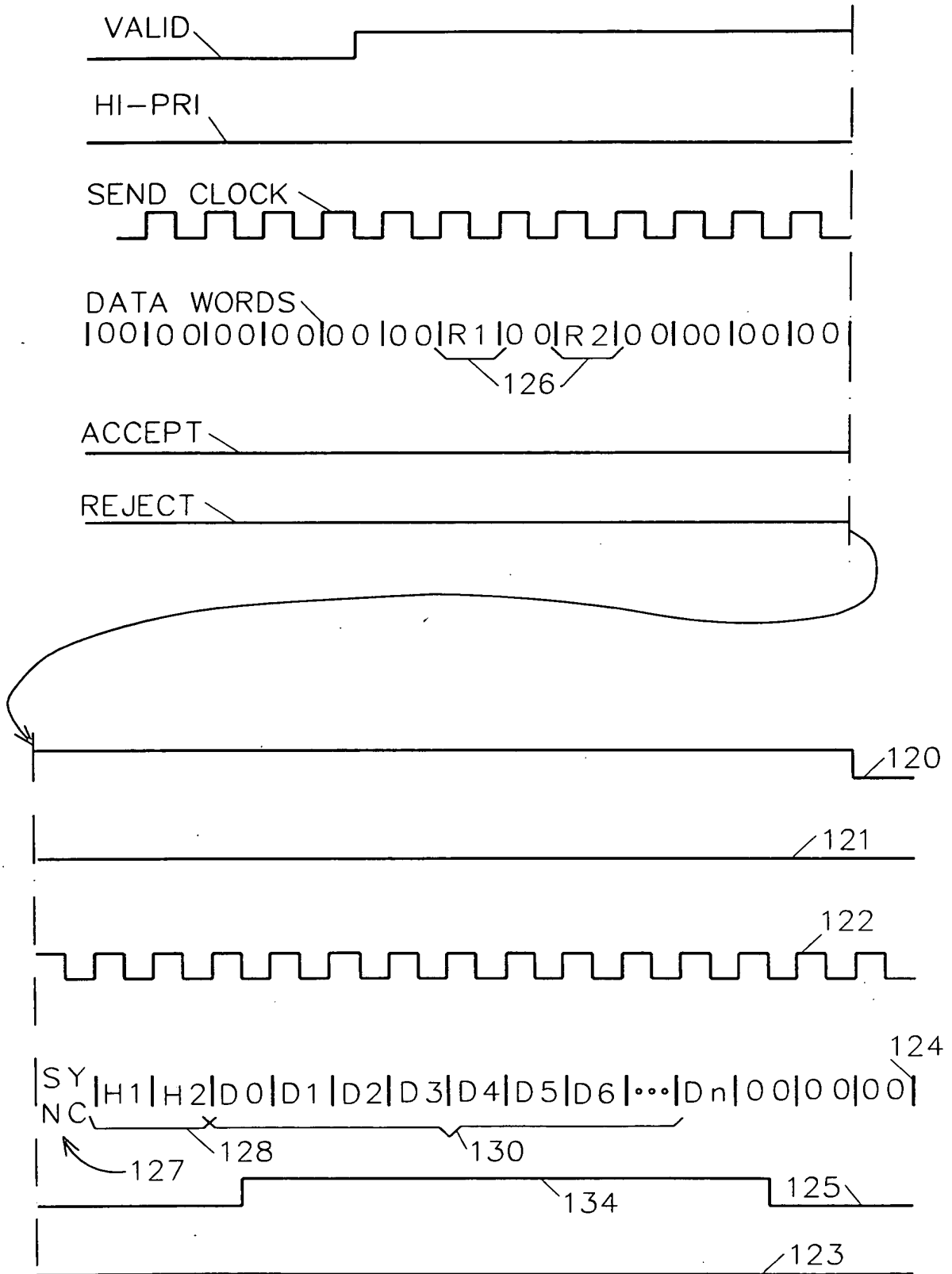
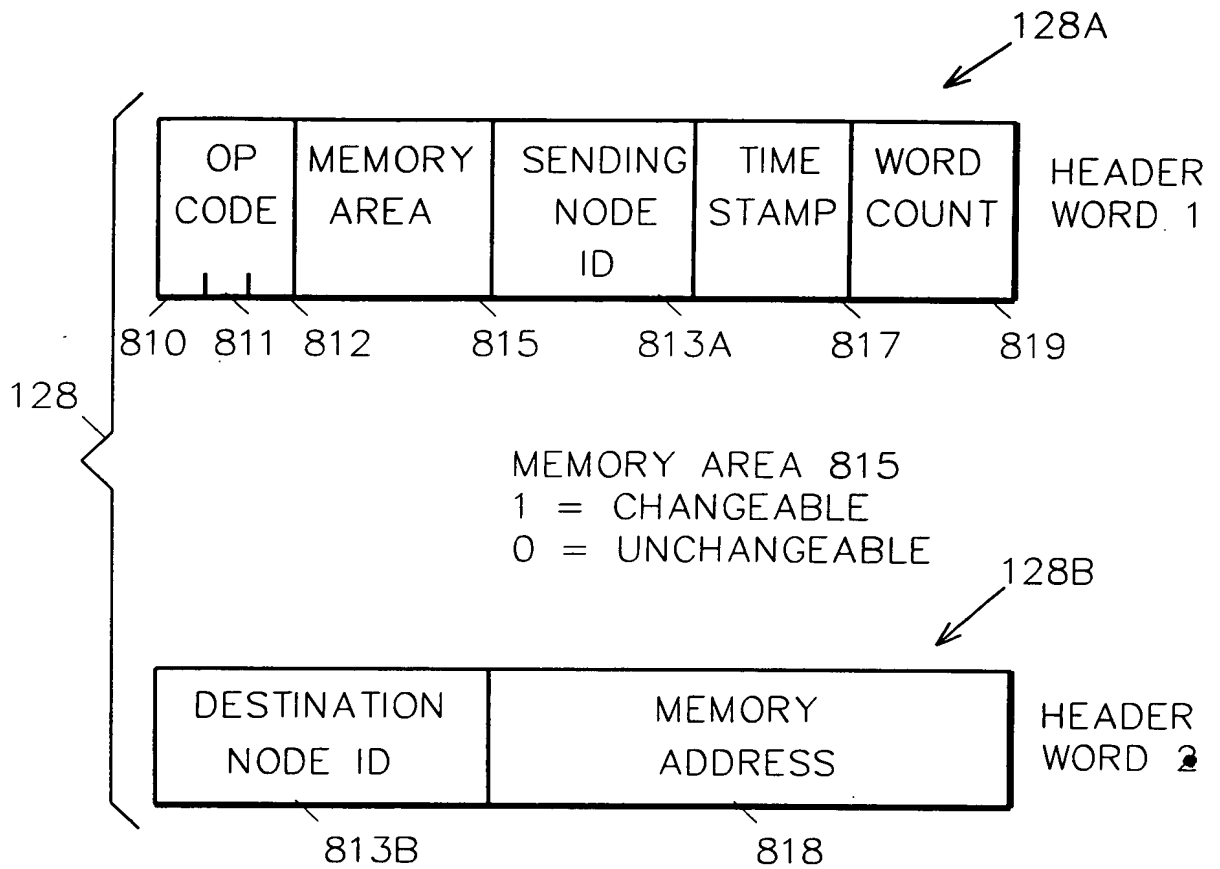


FIG. 6



OP CODE BITS			OP
810	811	812	
0	0	0	NO OP
0	0	1	READ
0	1	0	STORE
0	1	1	RESPONSE
1	0	0	NODE ID ASSIGN
1	0	1	INVALIDATION
1	1	0	CAST OUT
1	1	1	CACHE UPDATE

FIG. 7

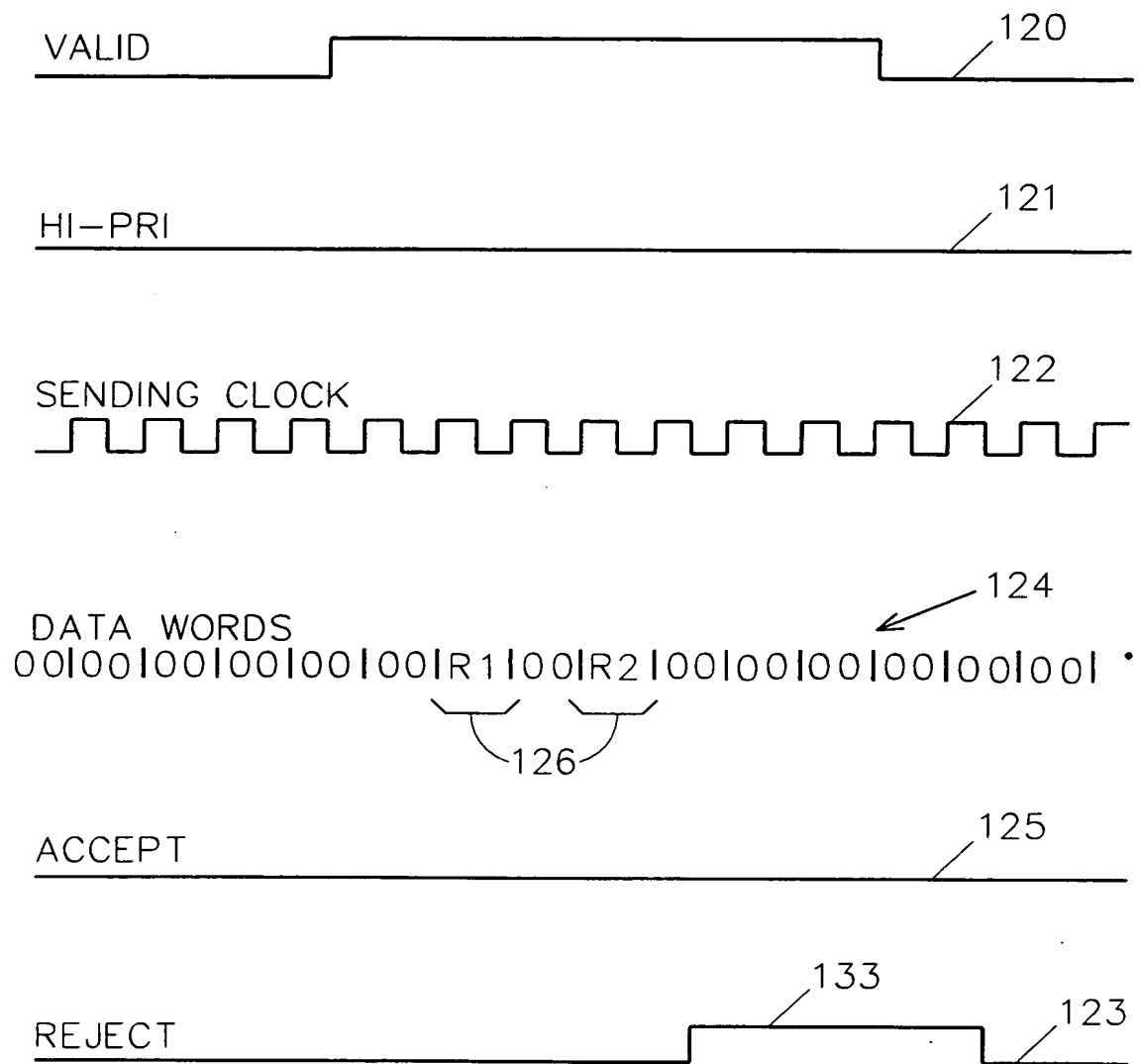


FIG. 8

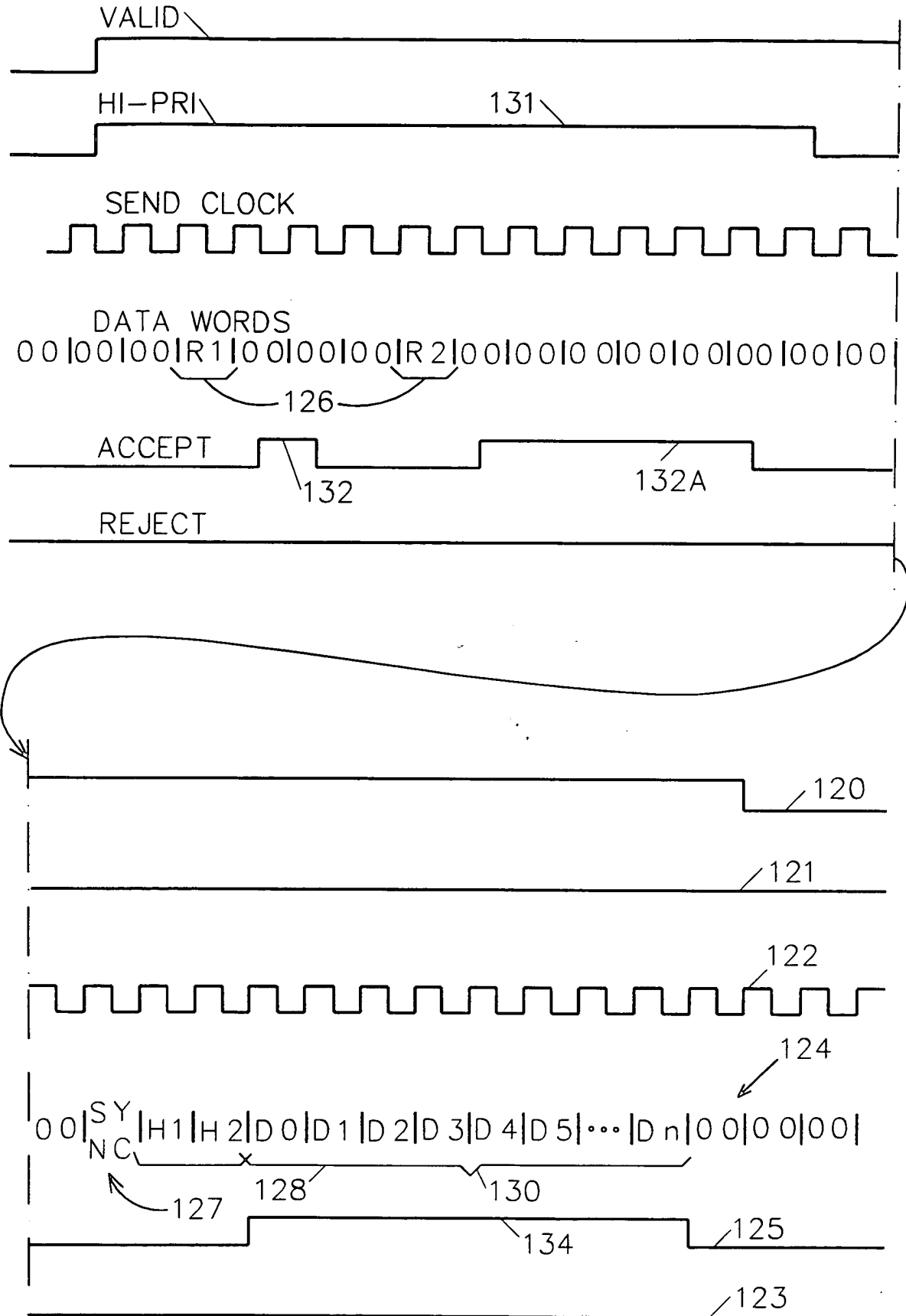


FIG. 9

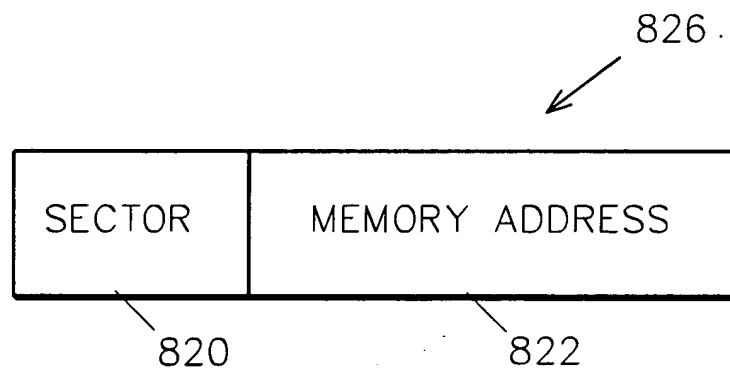


FIG. 10

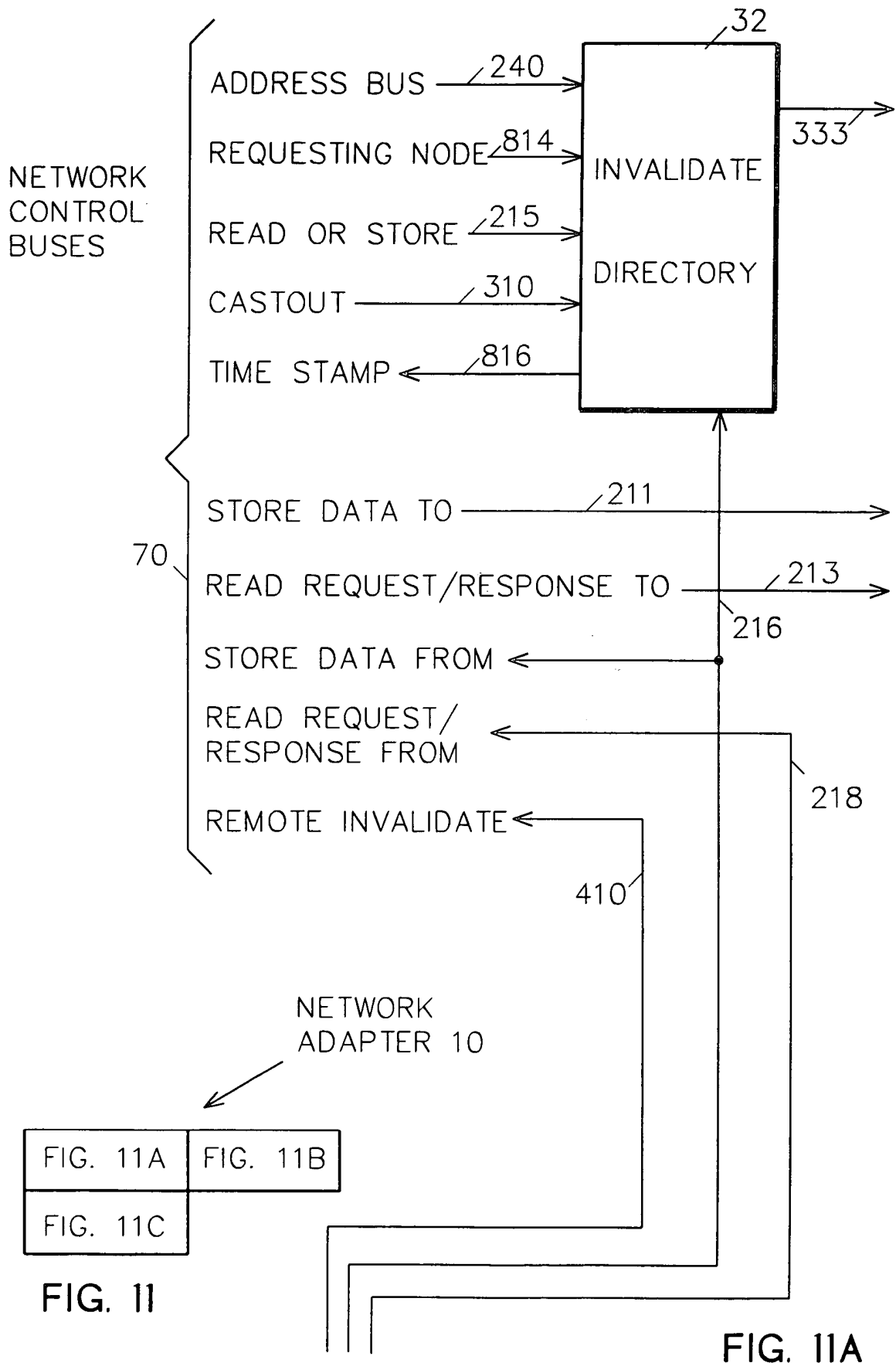


FIG. 11

FIG. 11A

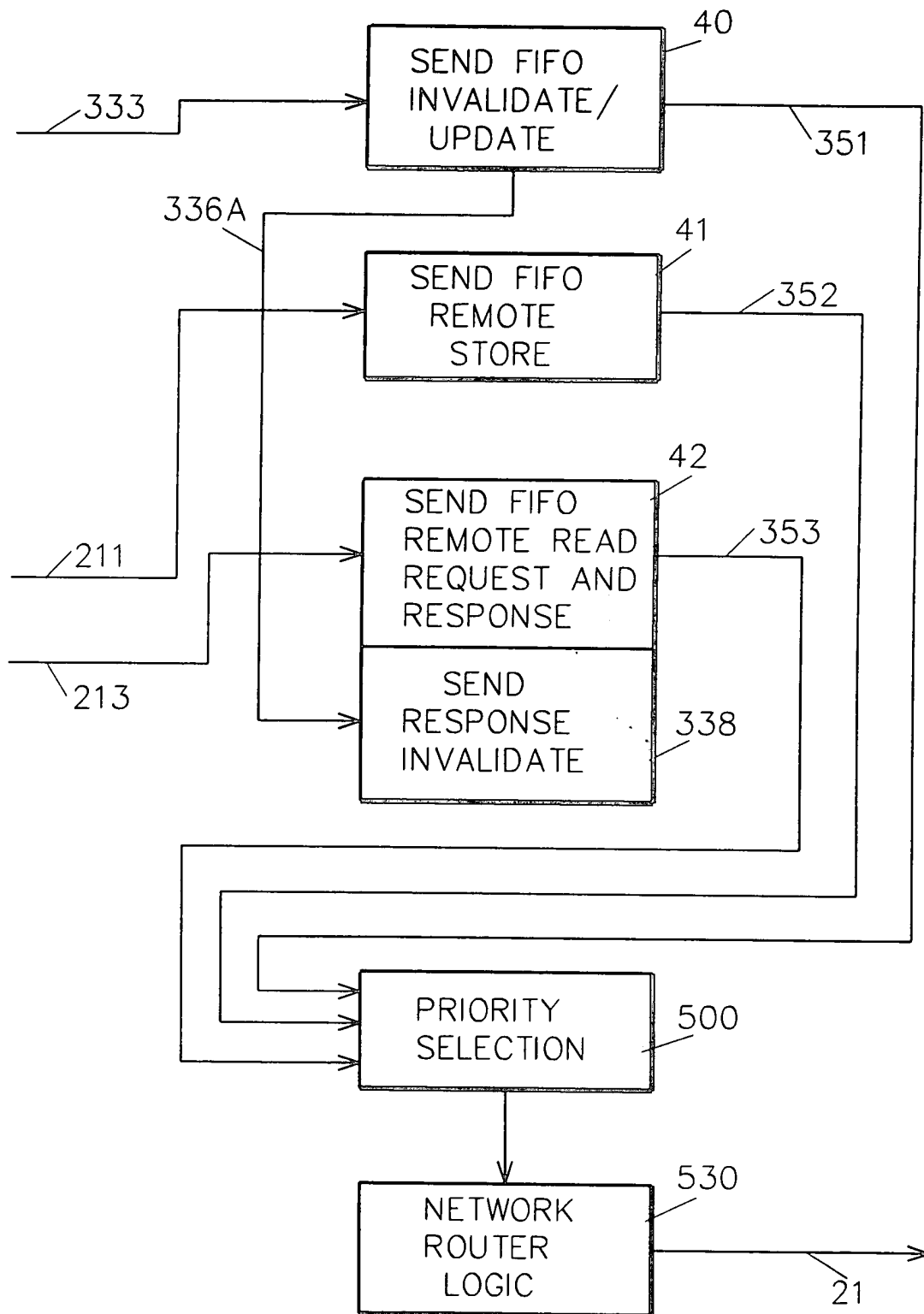


FIG. 11B

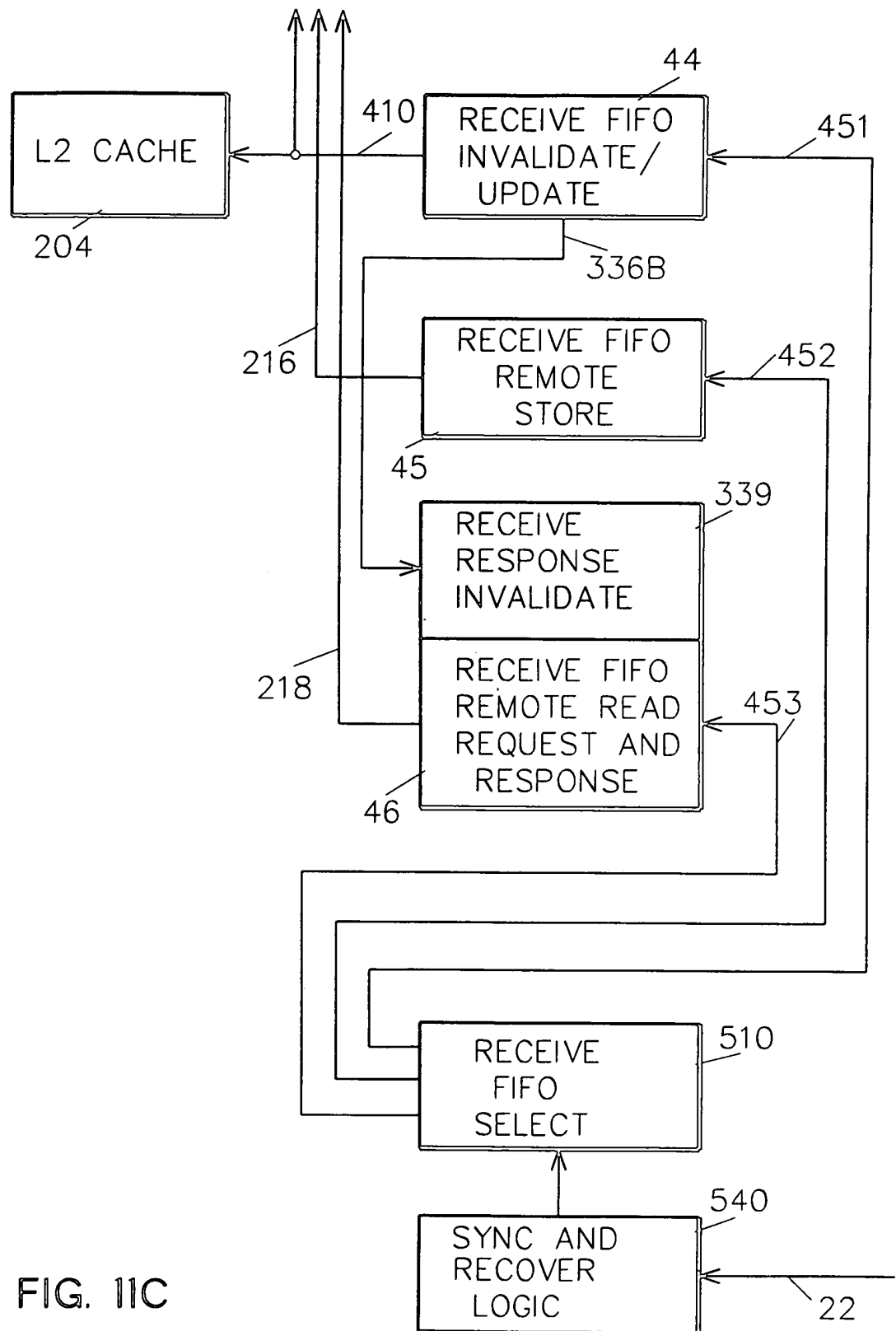


FIG. 11C

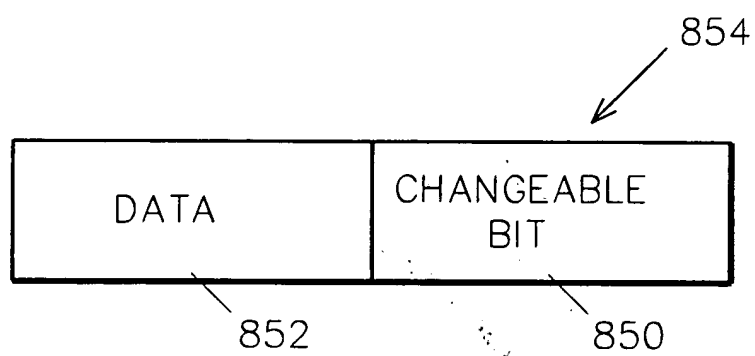


FIG. 12

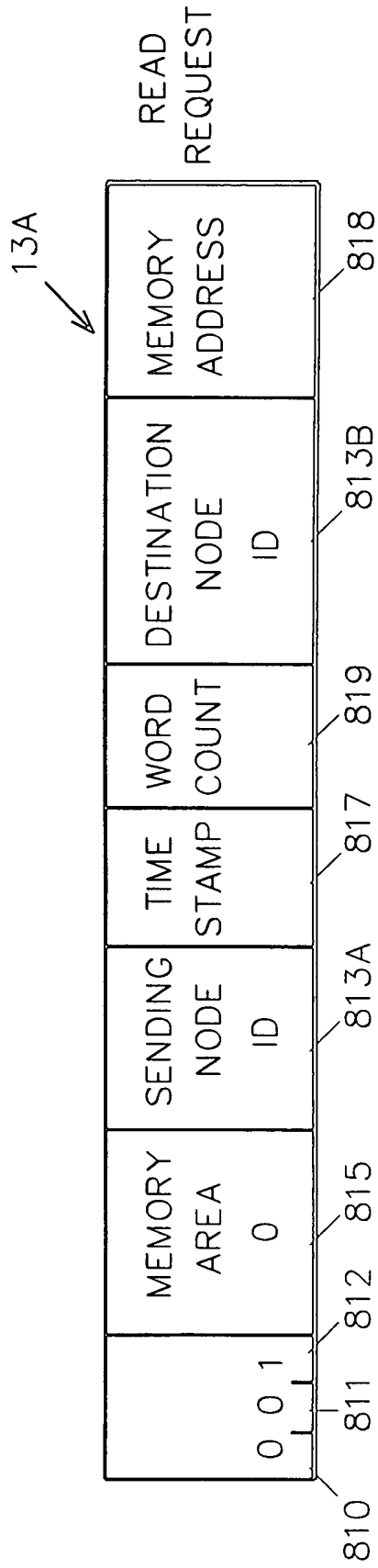


FIG. 13A

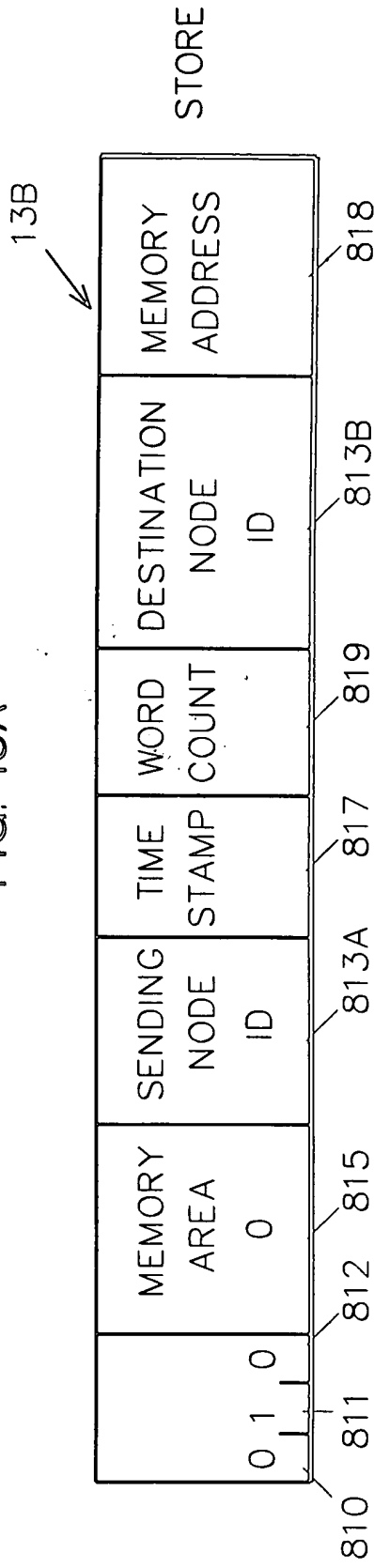


FIG. 13B

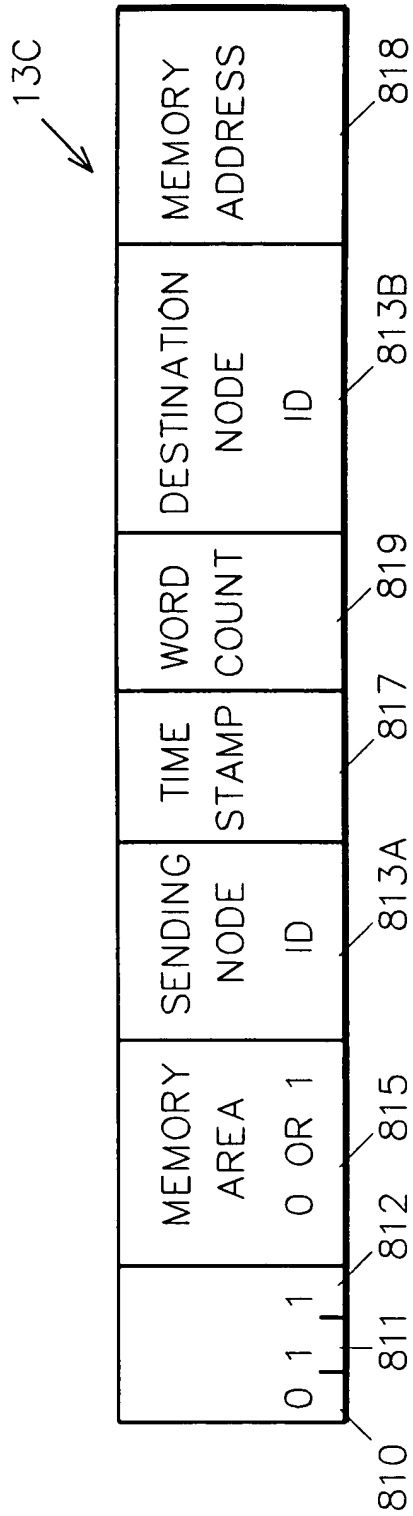


FIG. 13C

RESPONSE

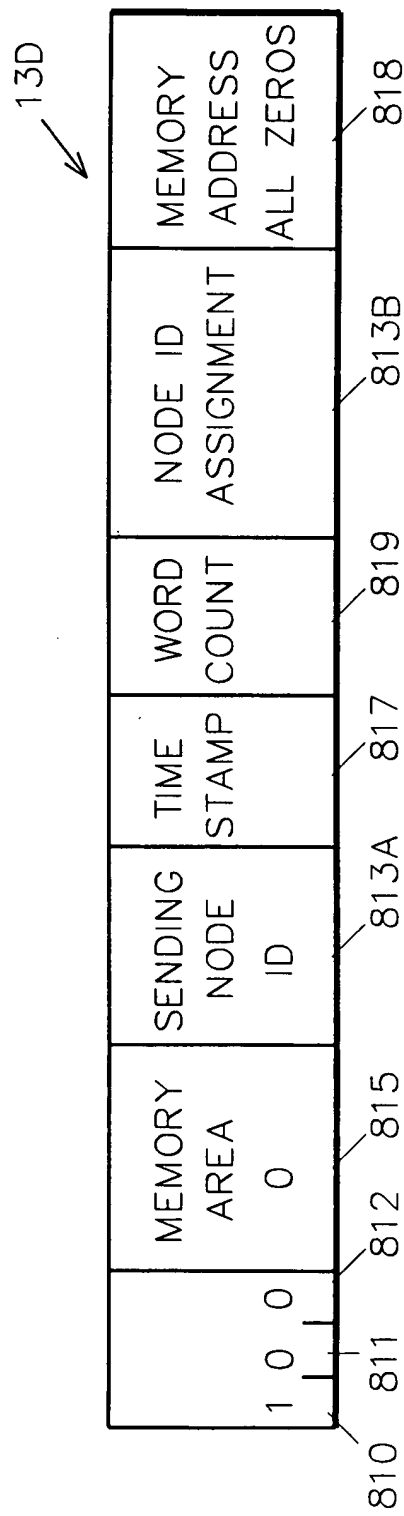


FIG. 13D

NODE ID ASSIGNMENT

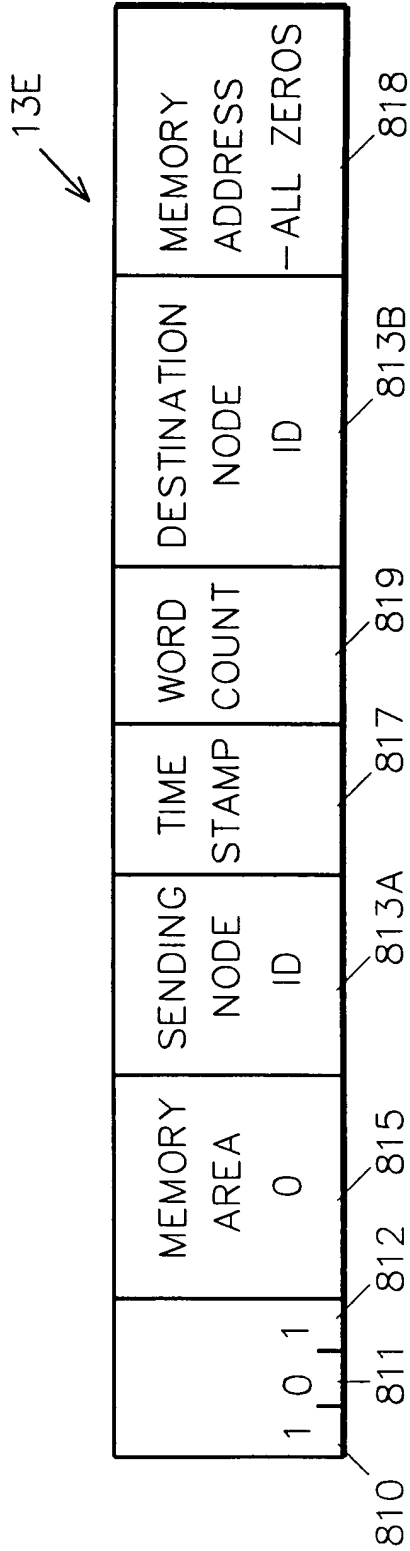


FIG. 13E

INVALIDATION

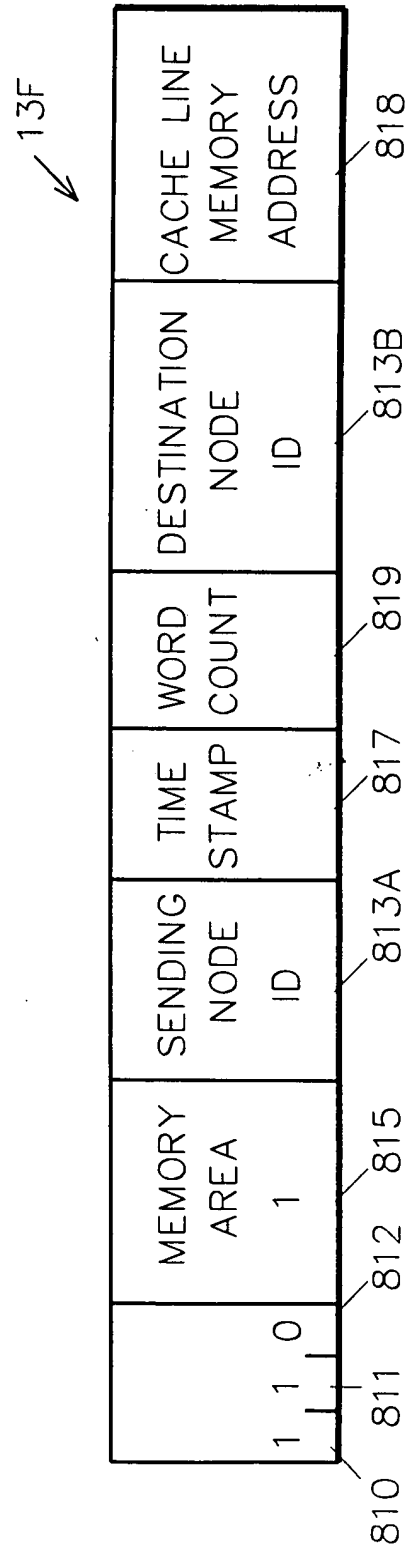


FIG. 13F

CAST OUT

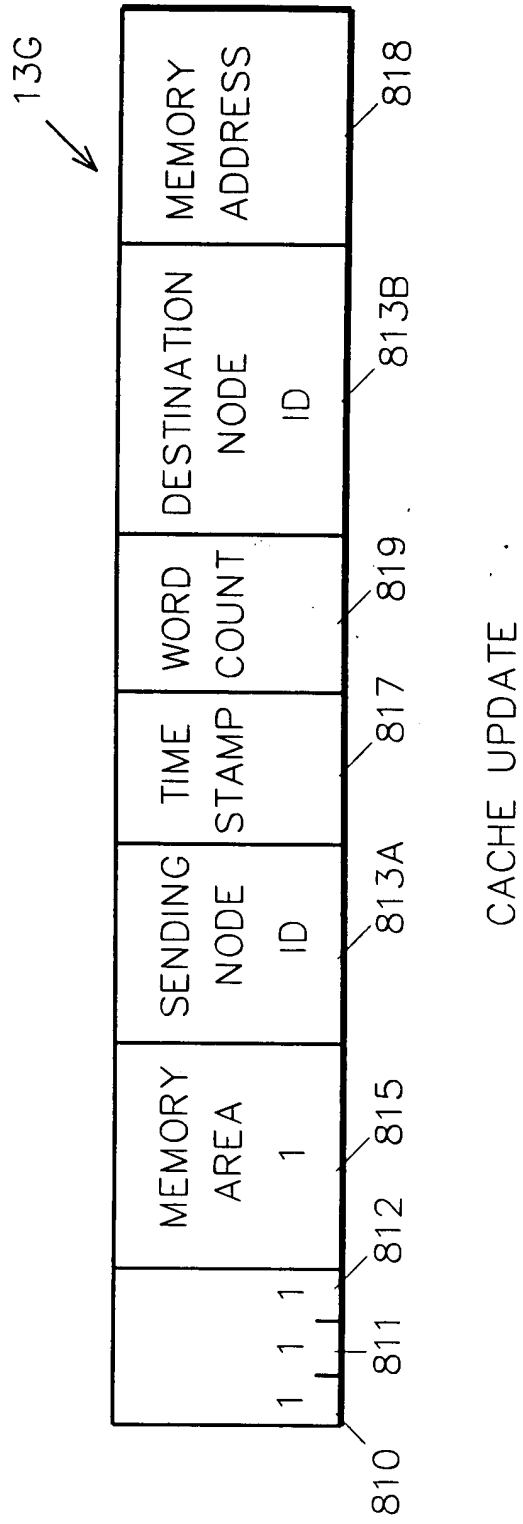


FIG. 13G

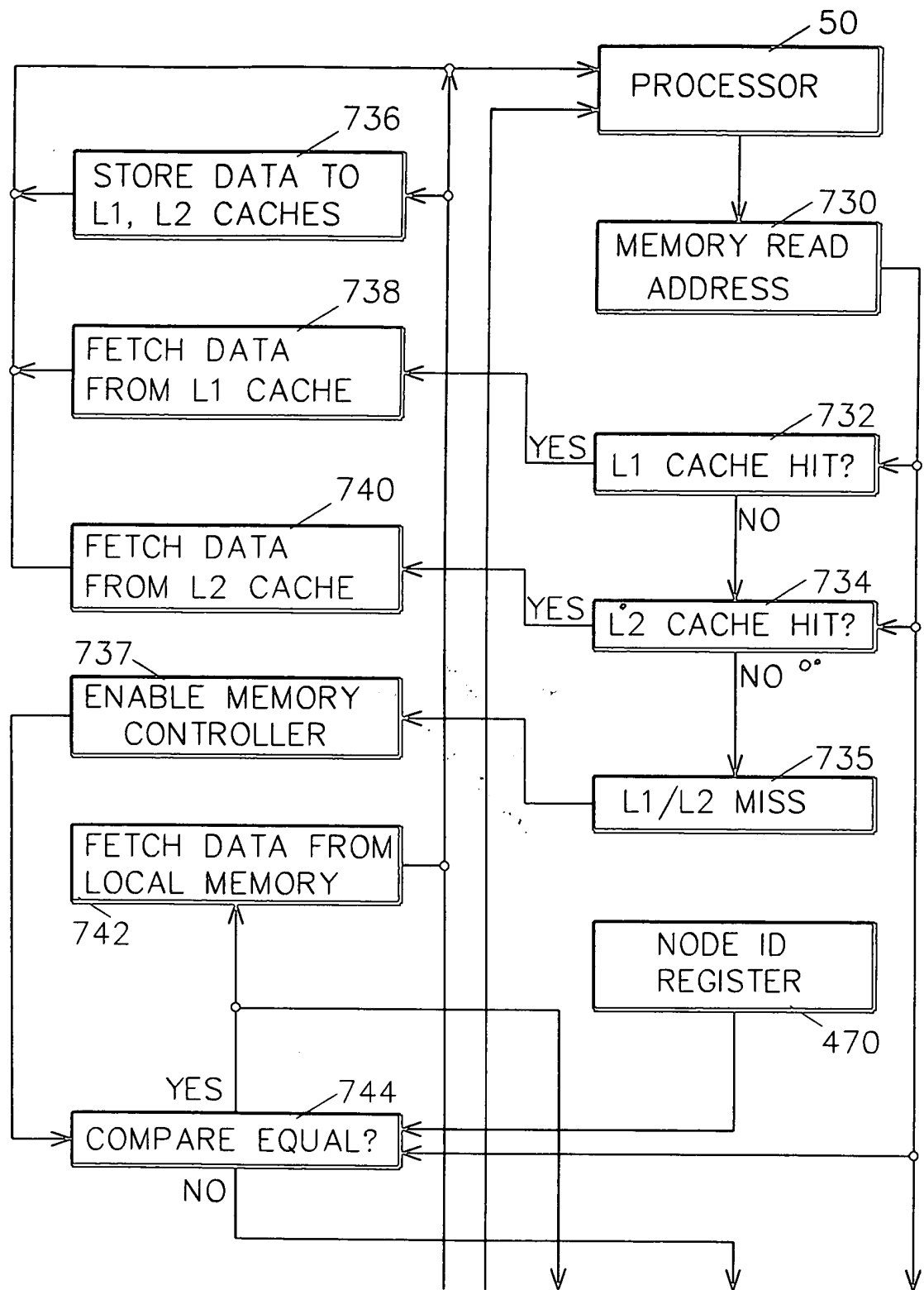


FIG. 14A

FIG. 14B

FIG. 14

FIG. 14A

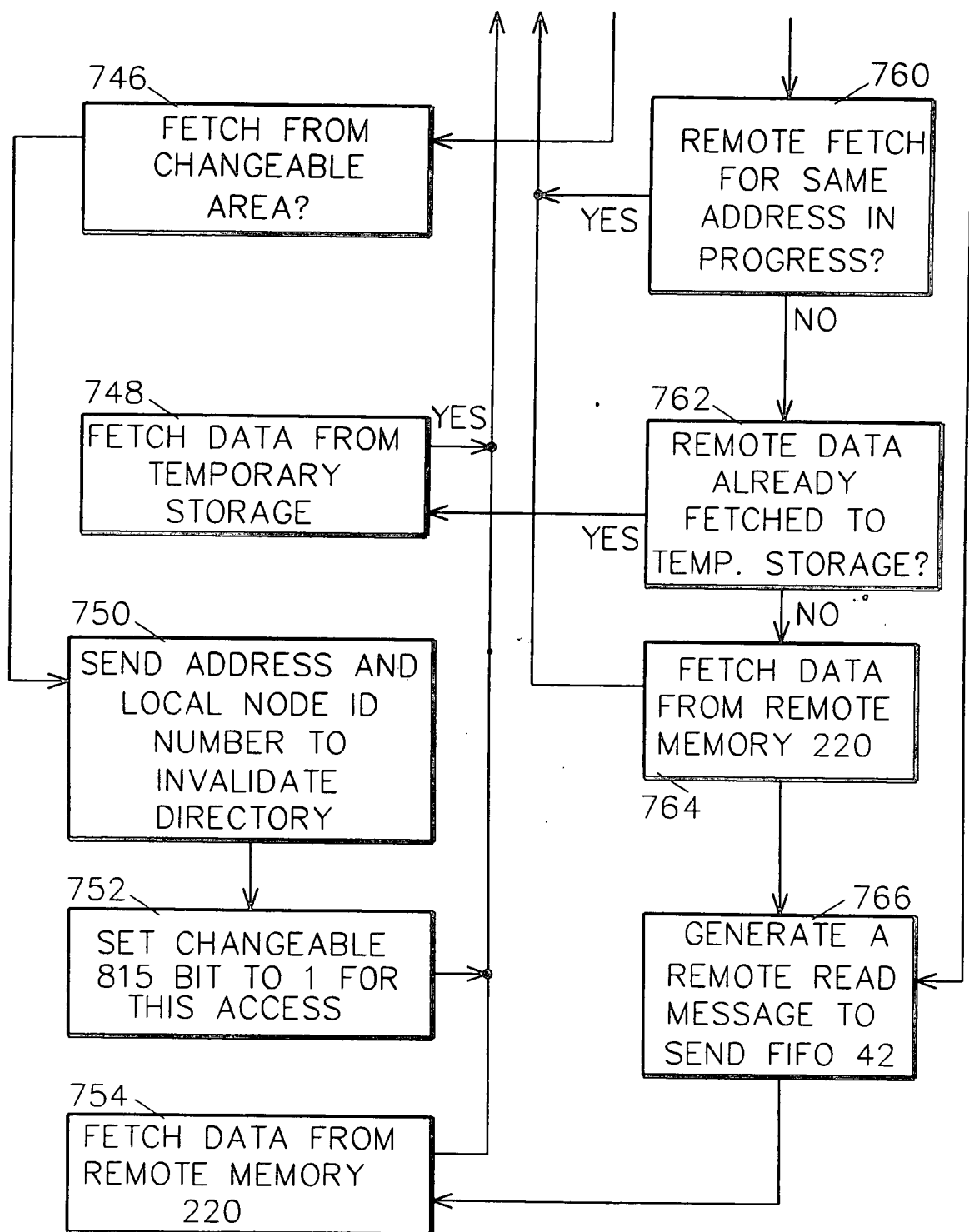
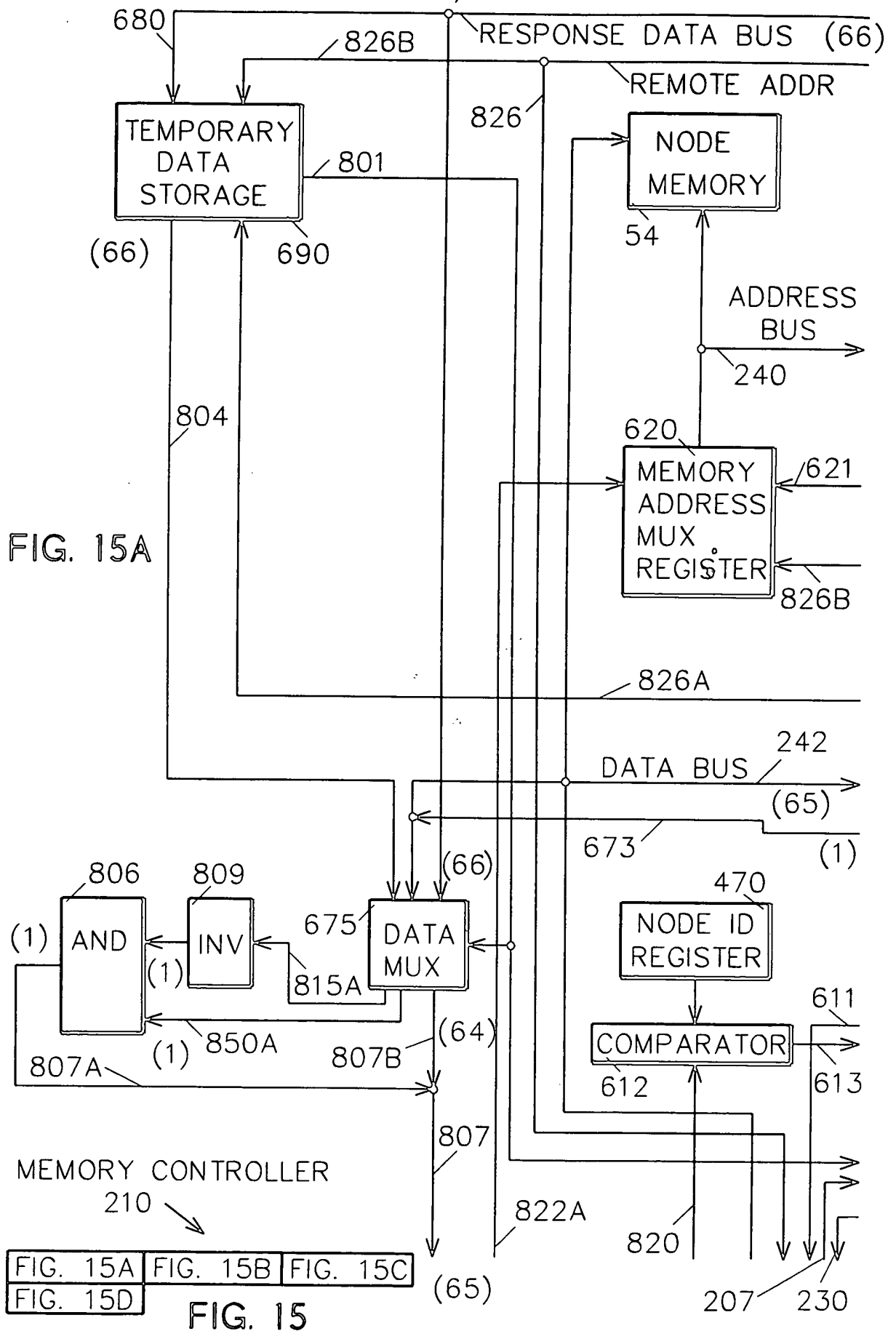


FIG. 14B



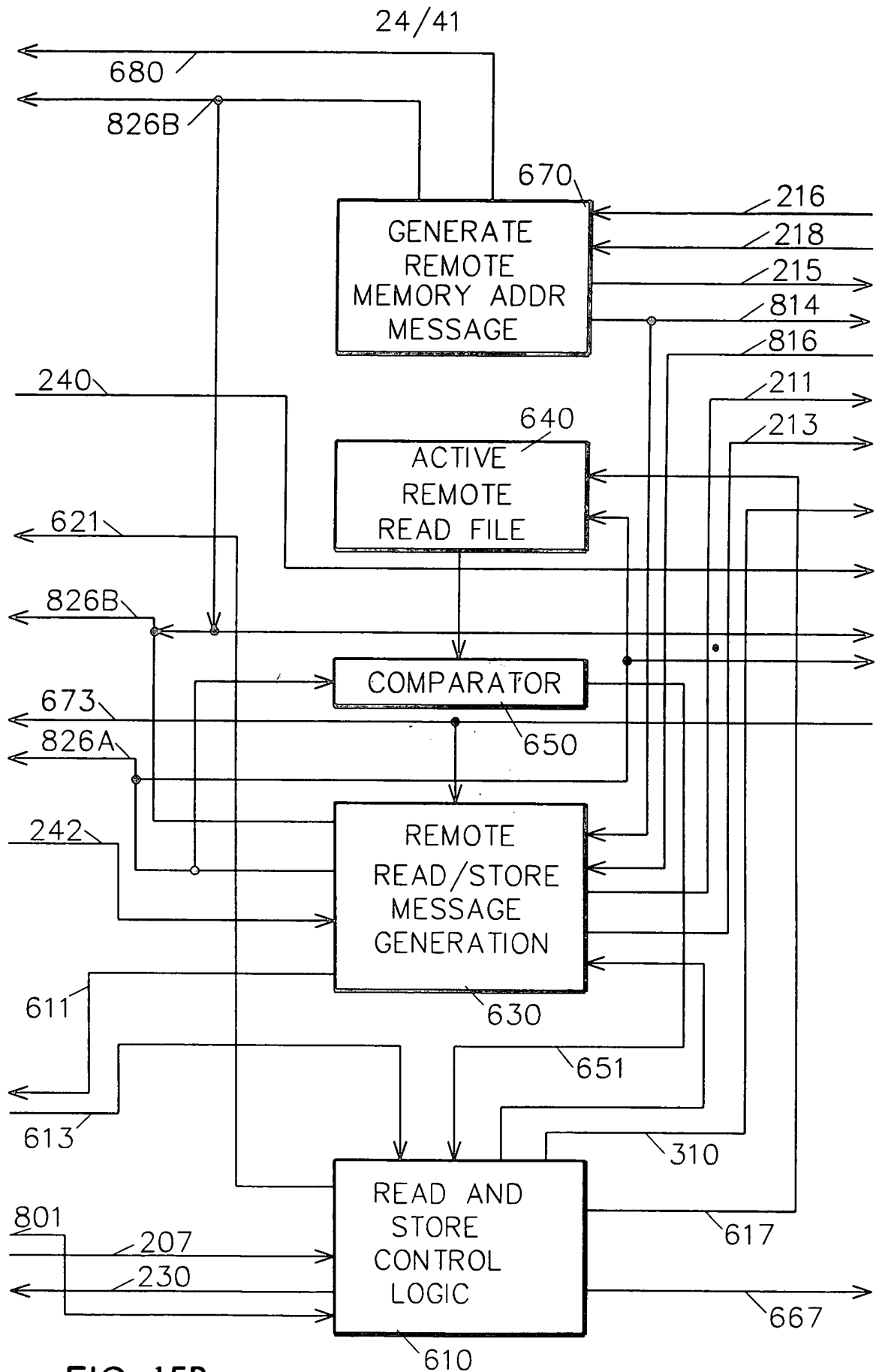


FIG. 15B

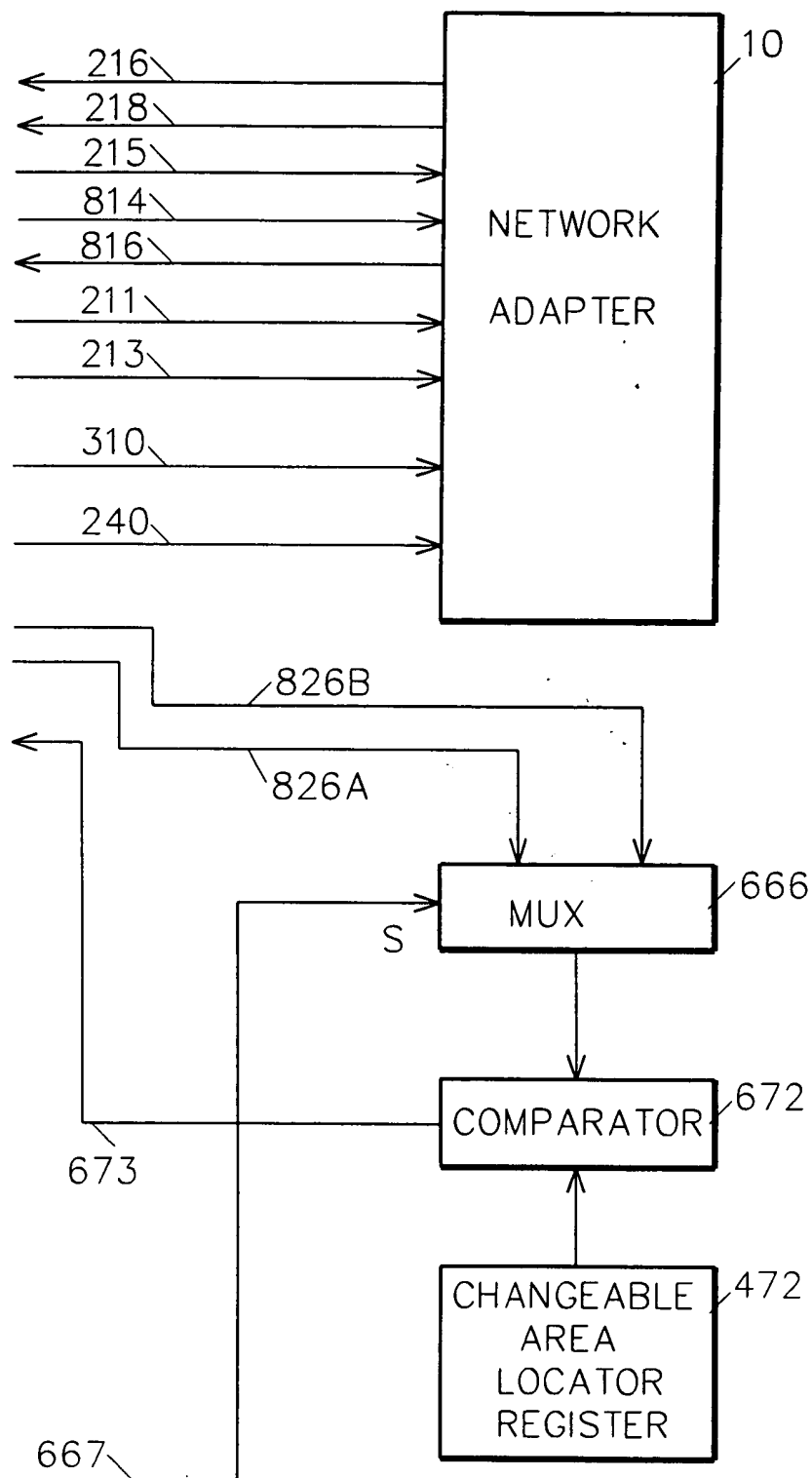


FIG. 15C

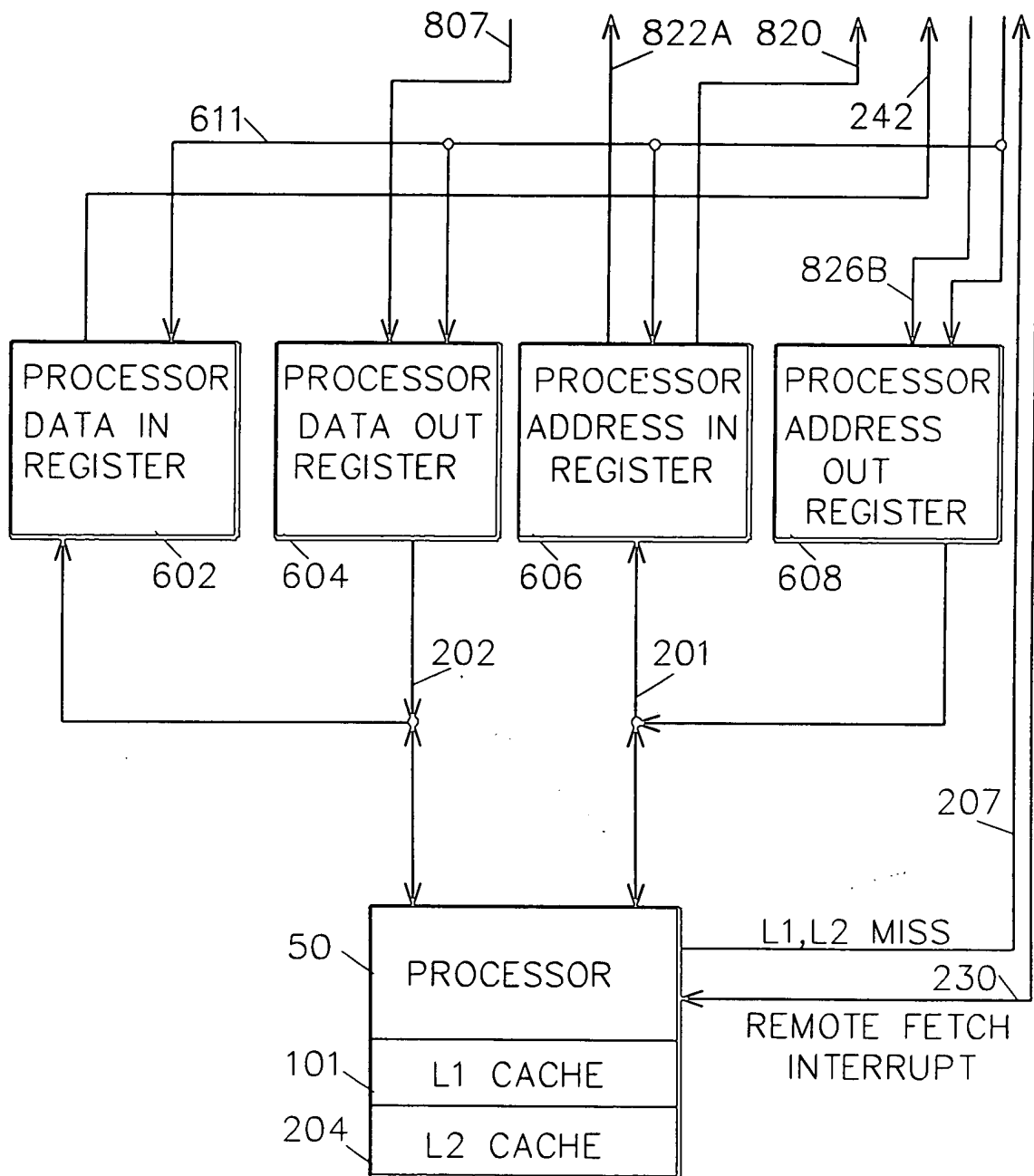


FIG. 15D

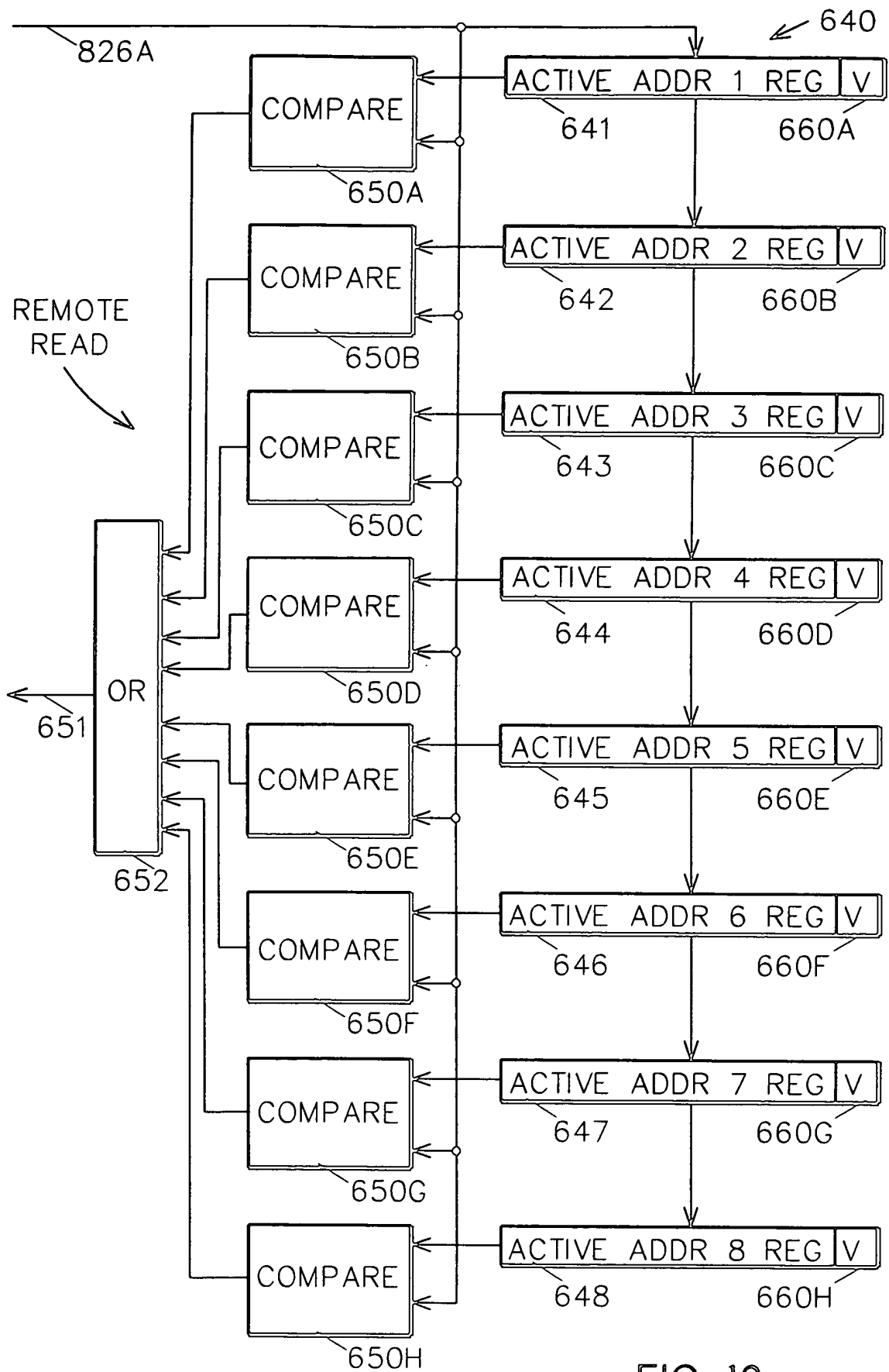


FIG. 16

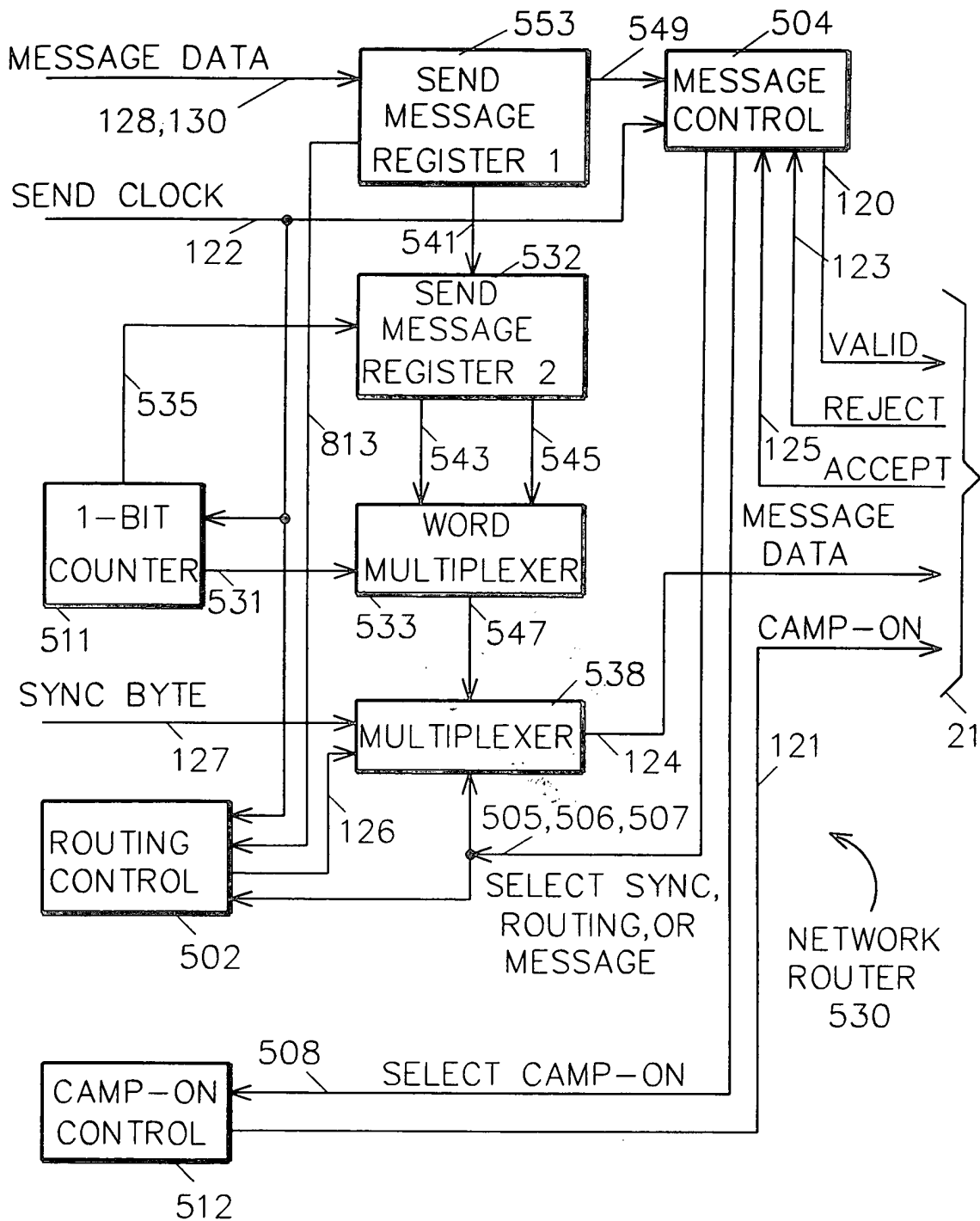


FIG. 17

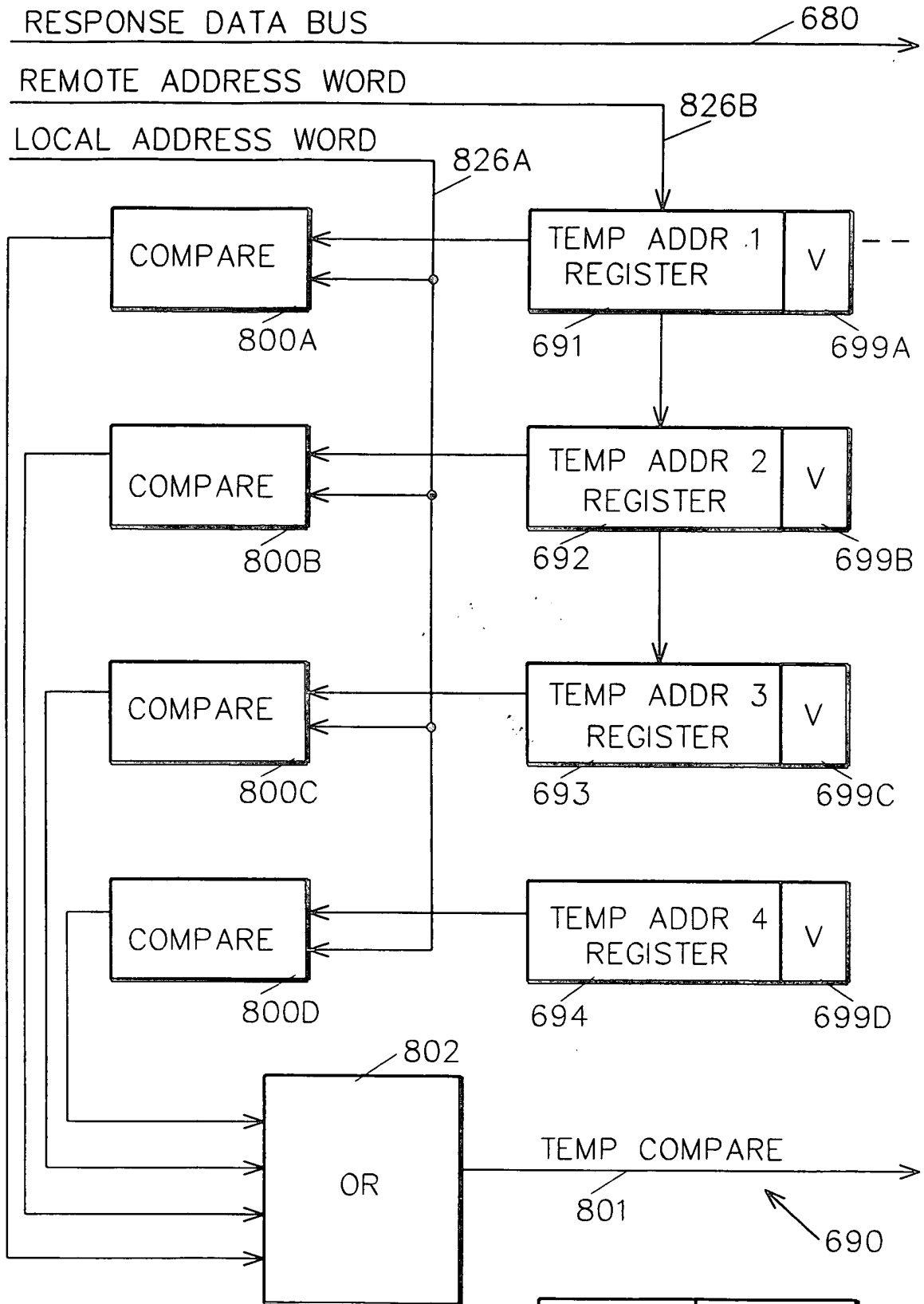


FIG. 18A	FIG. 18B
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FIG. 18A

FIG. 18

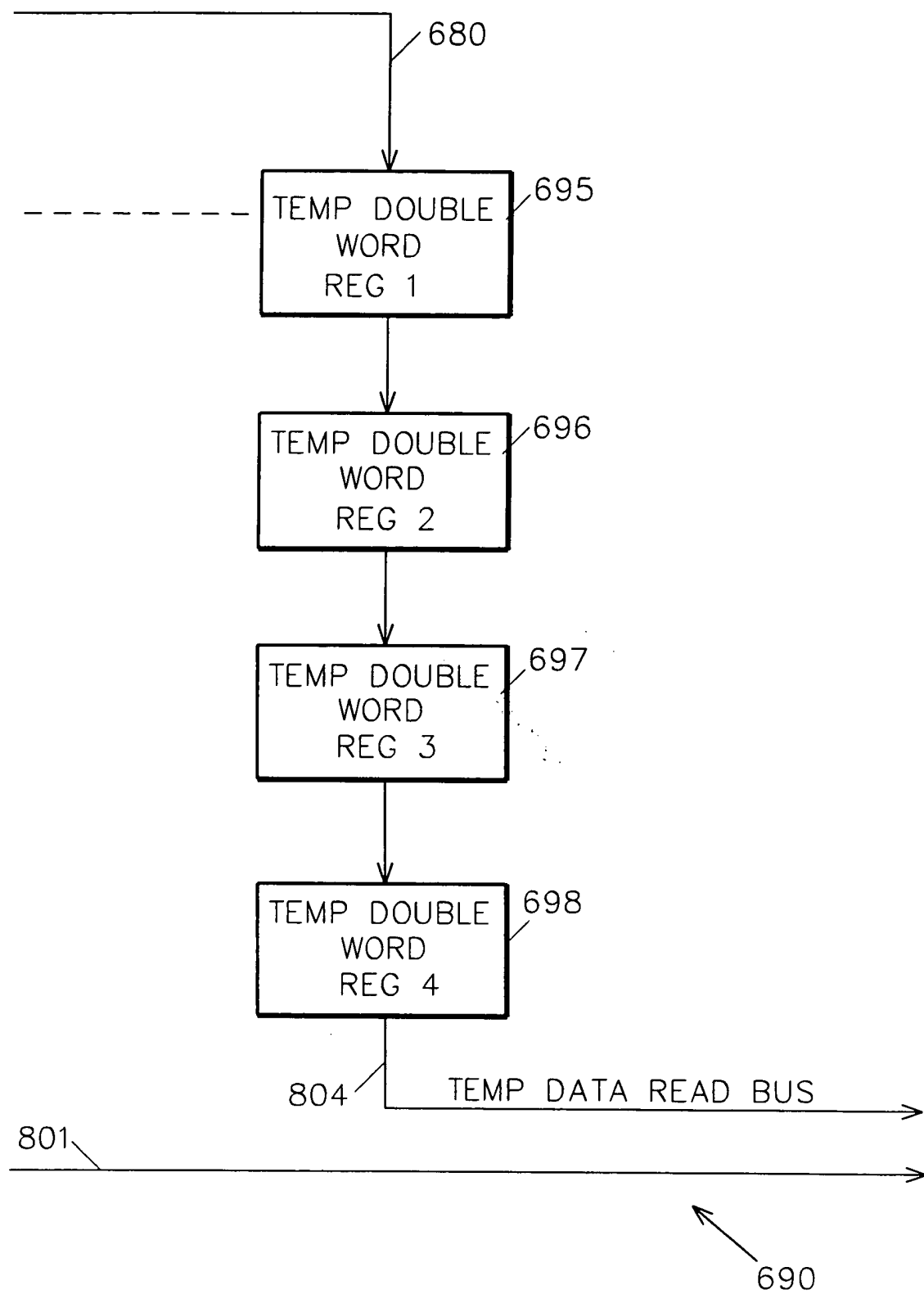


FIG. 18B

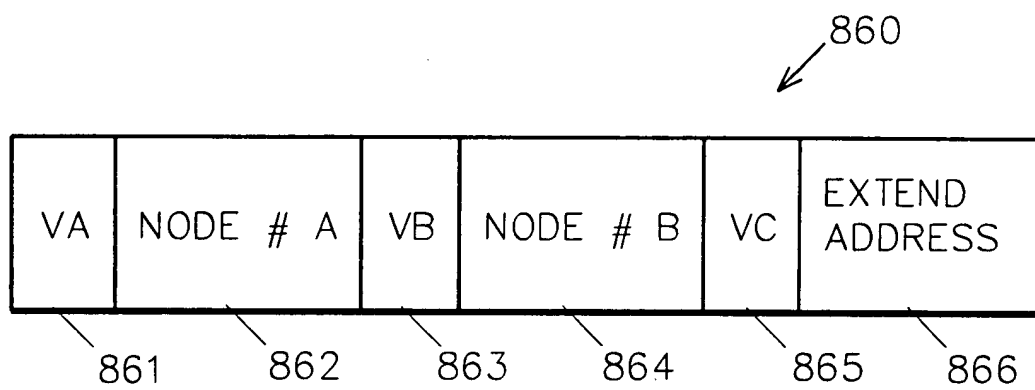
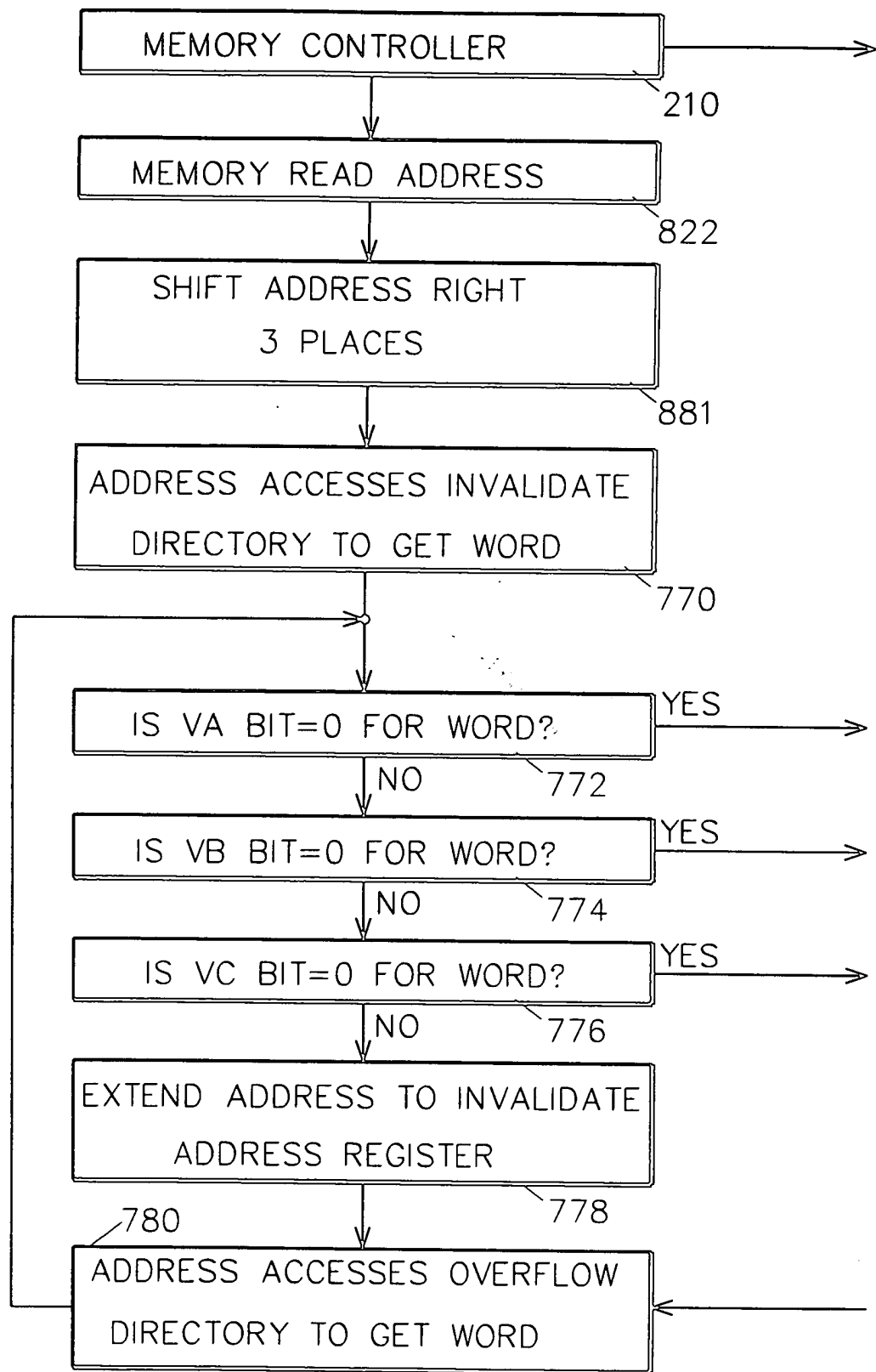


FIG. 19



20A	20B
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FIG. 20A

FIG. 20

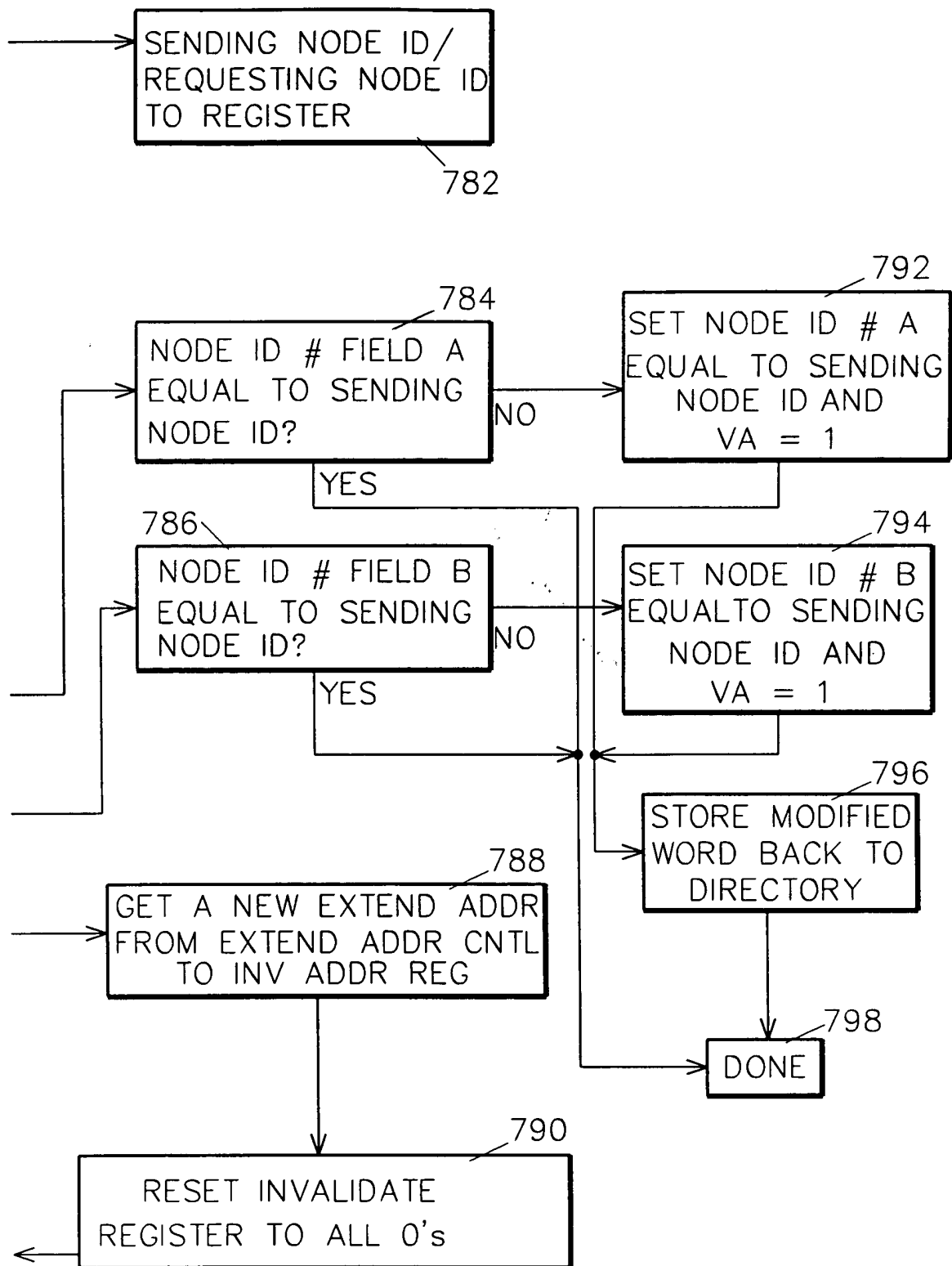


FIG. 20B

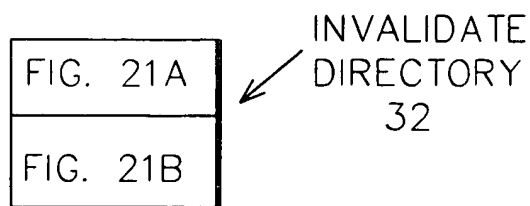
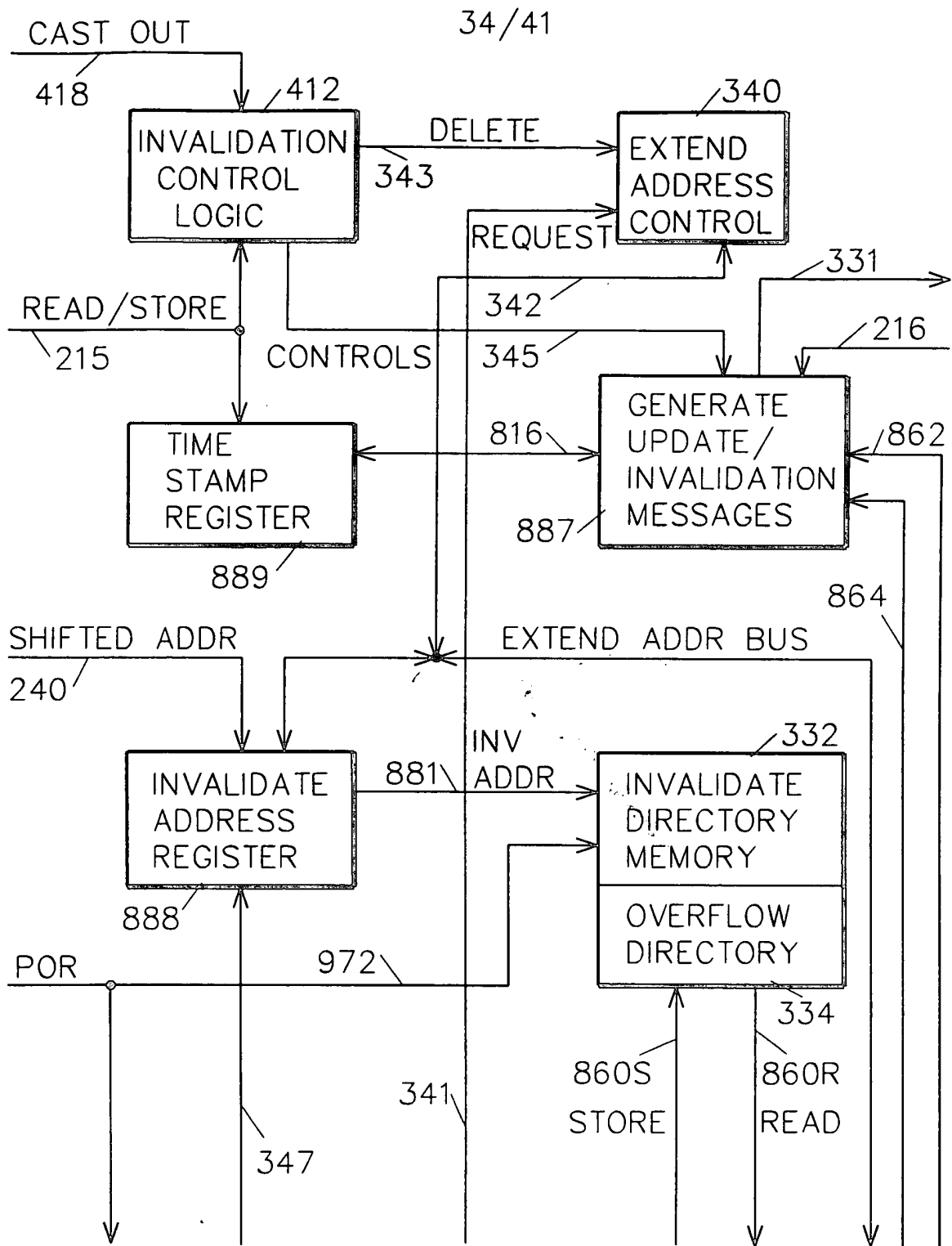


FIG. 21

FIG. 21A

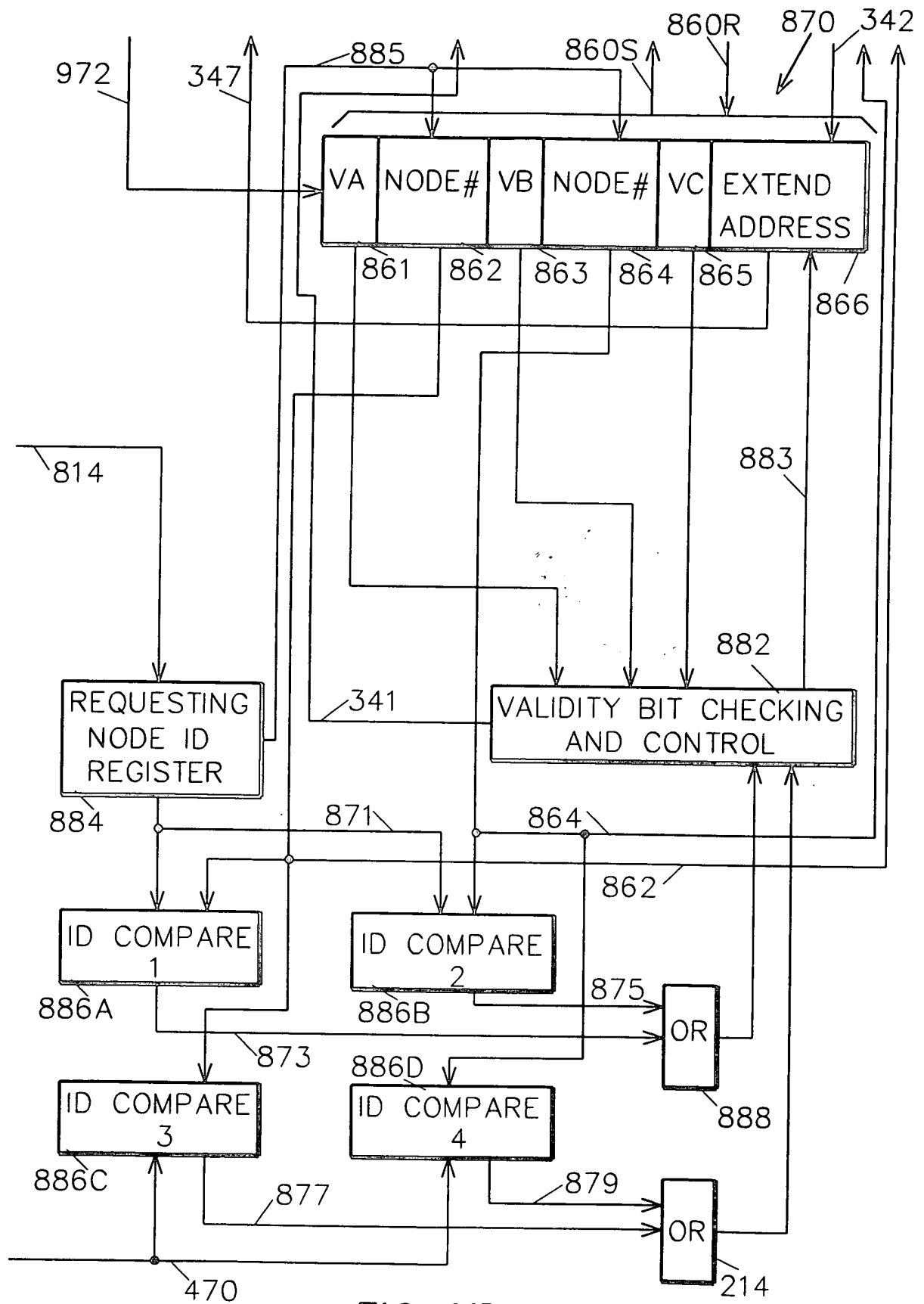


FIG. 21B

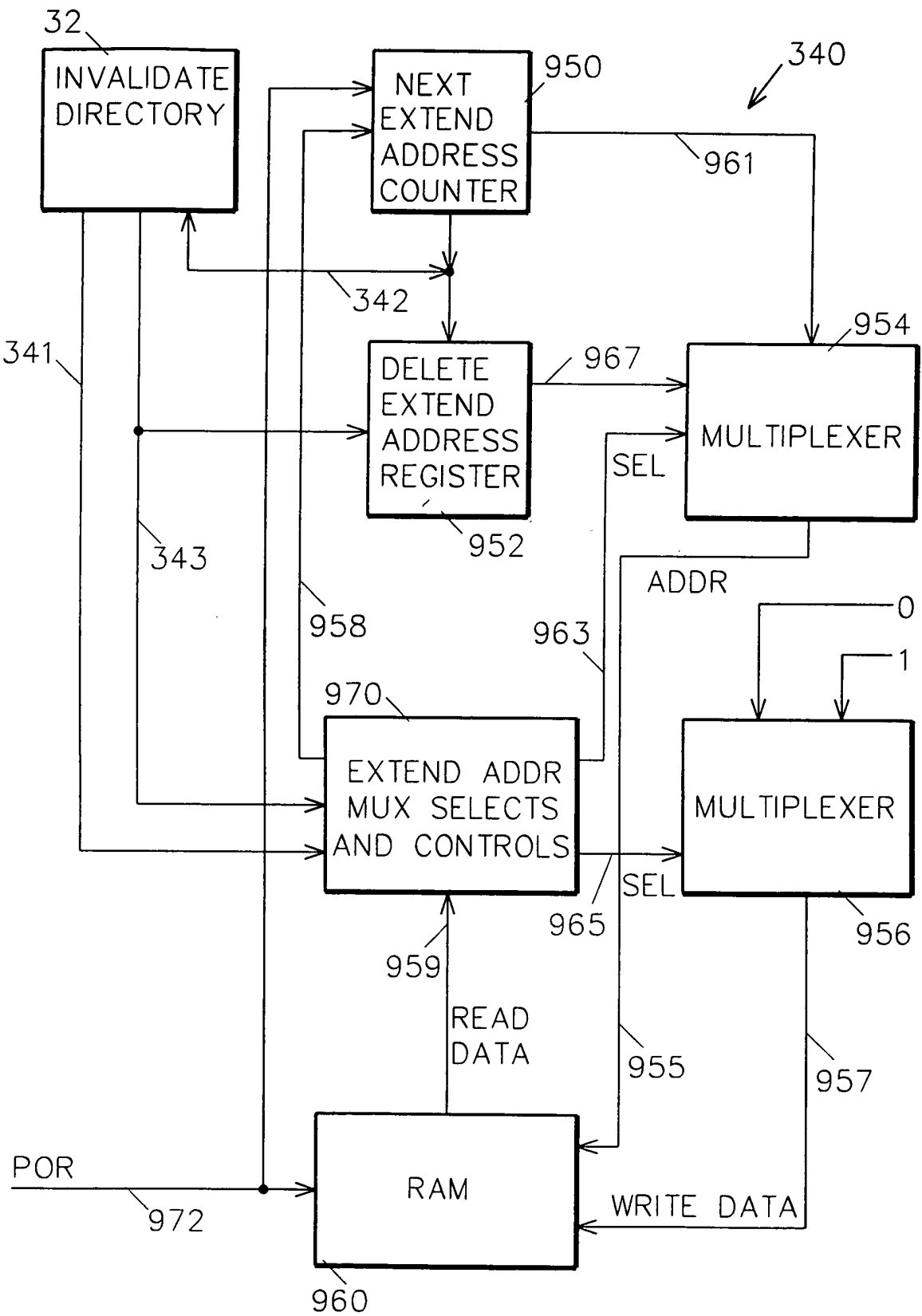


FIG. 22

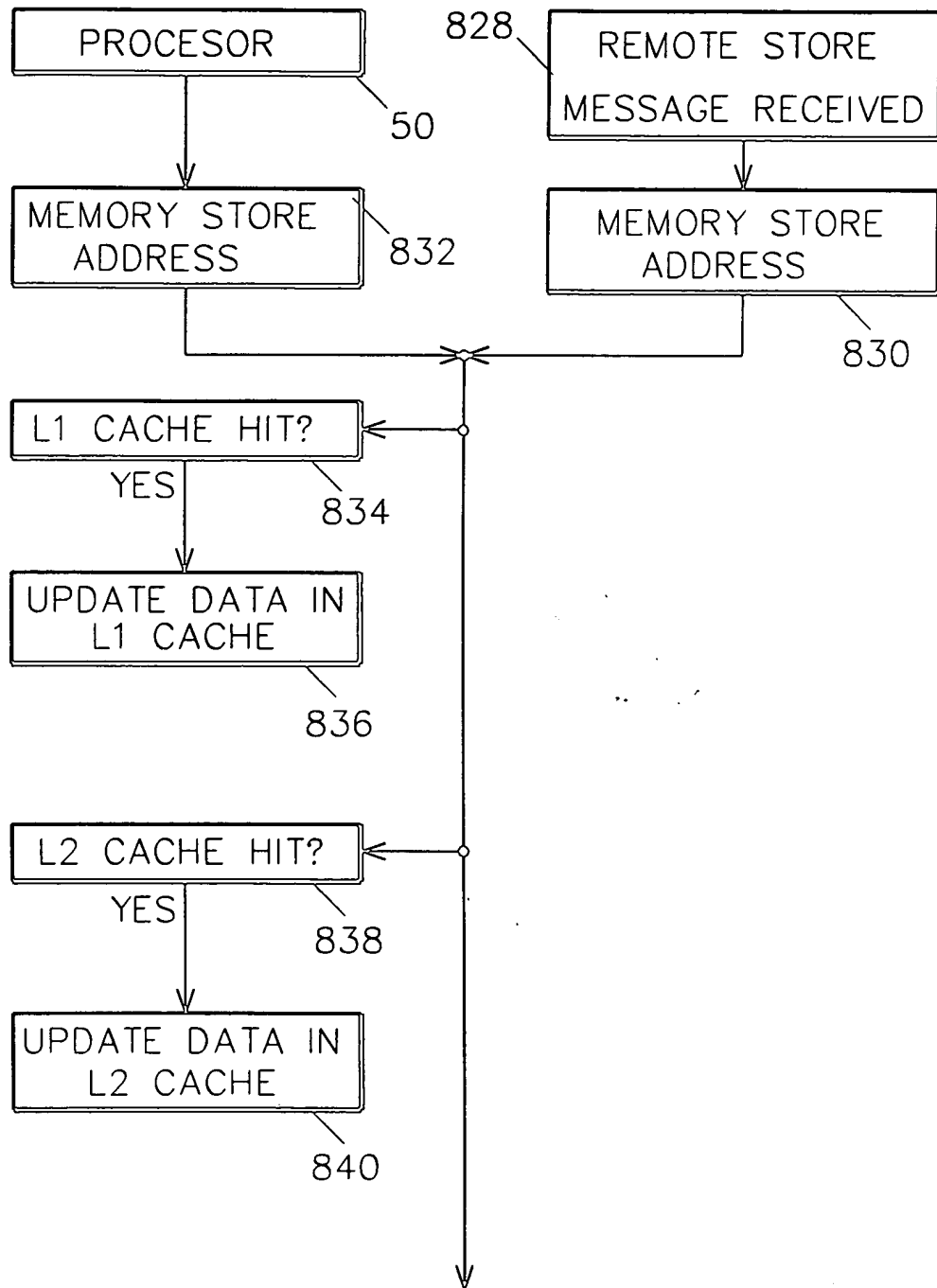


FIG. 23A

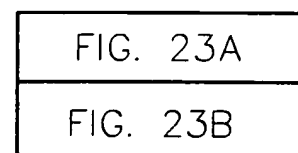


FIG. 23

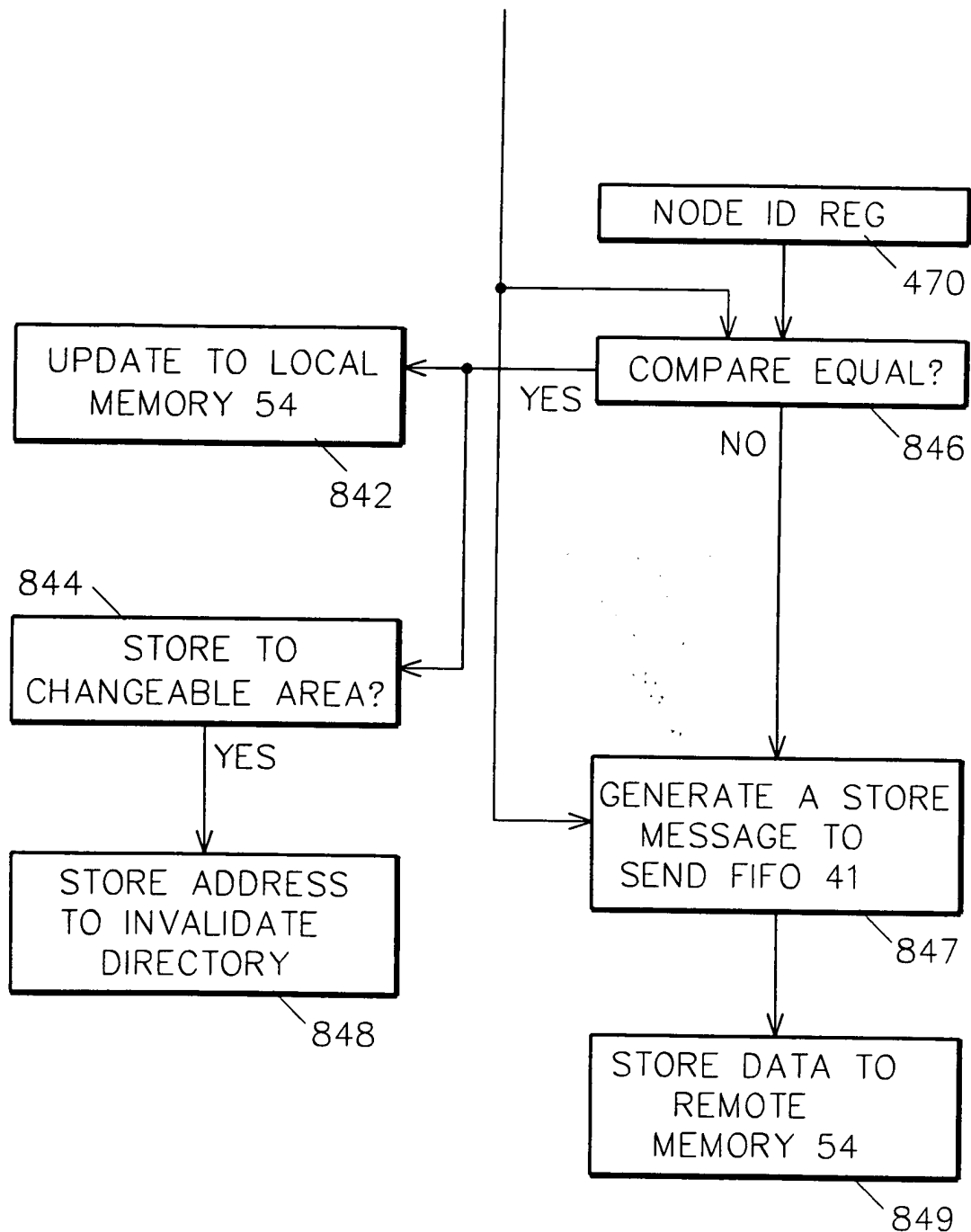


FIG. 23B

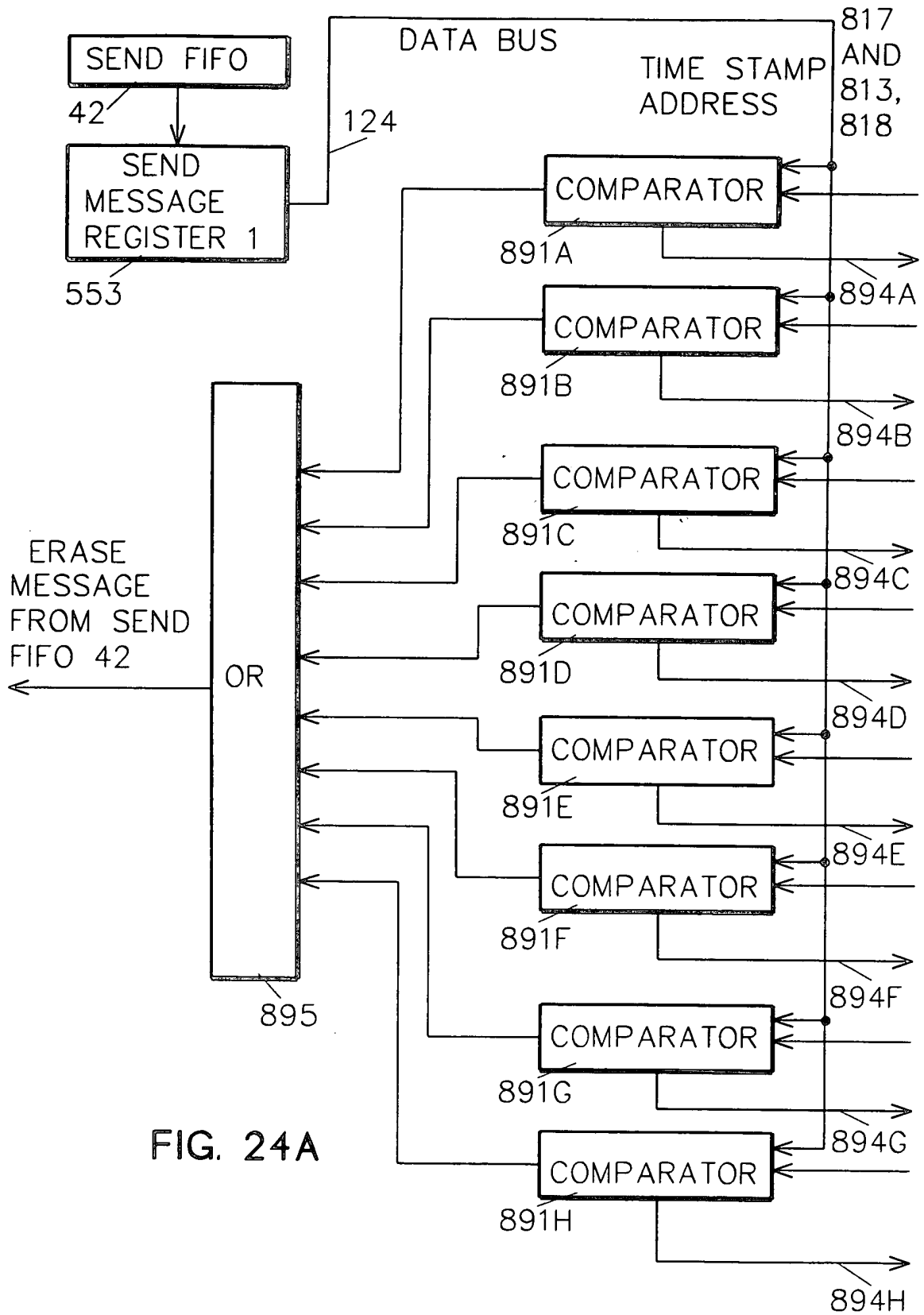


FIG. 24A

FIG. 24A	FIG. 24B	FIG. 24C
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FIG. 24

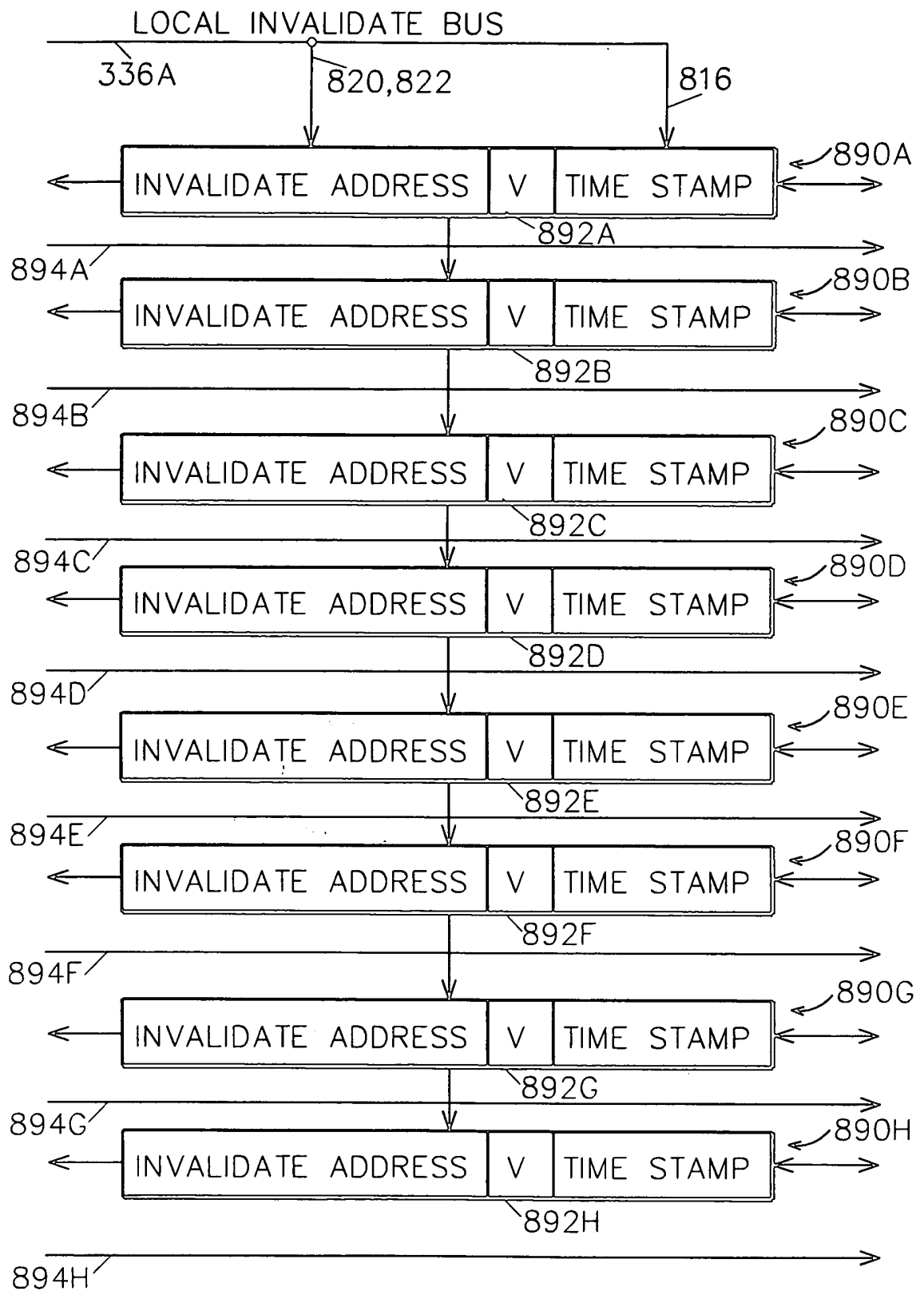


FIG. 24B

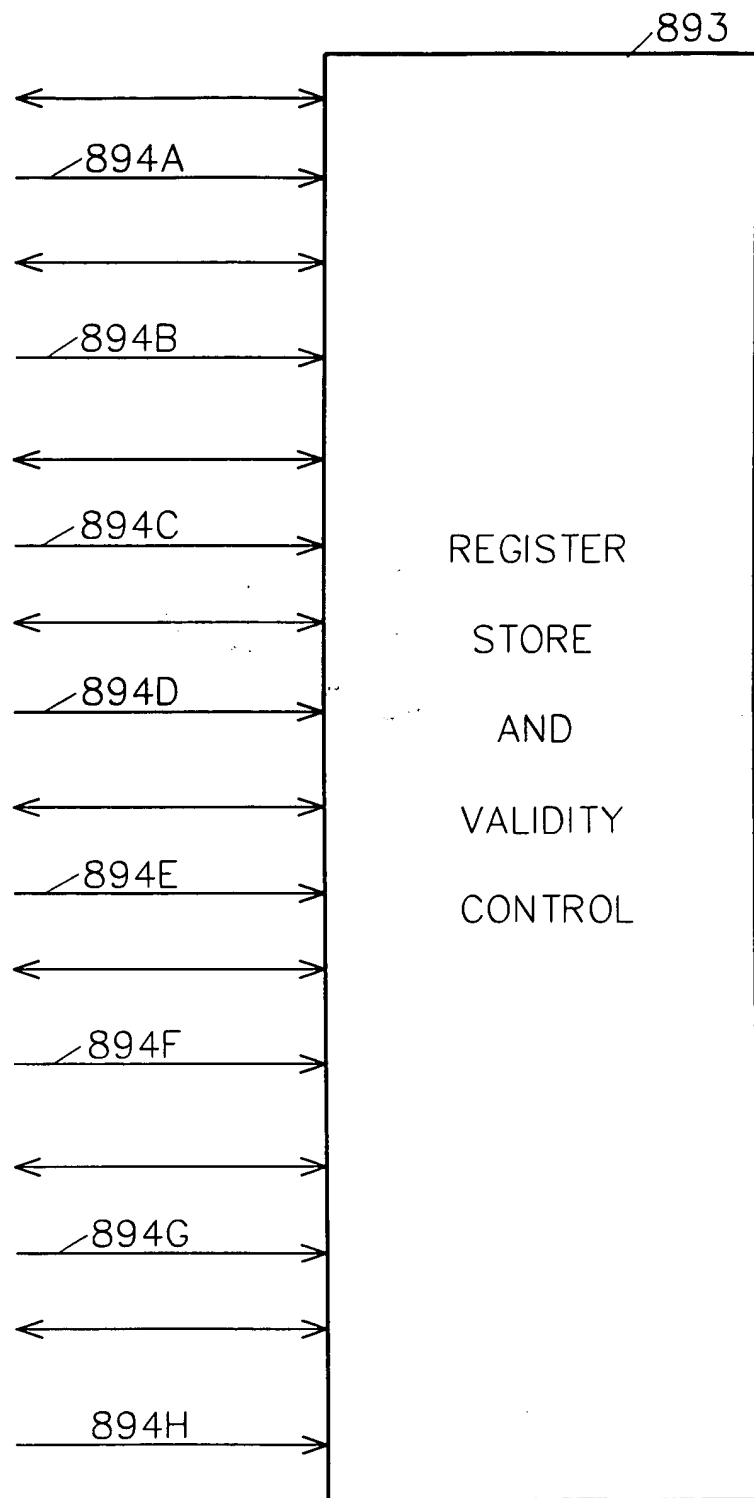


FIG. 24C